

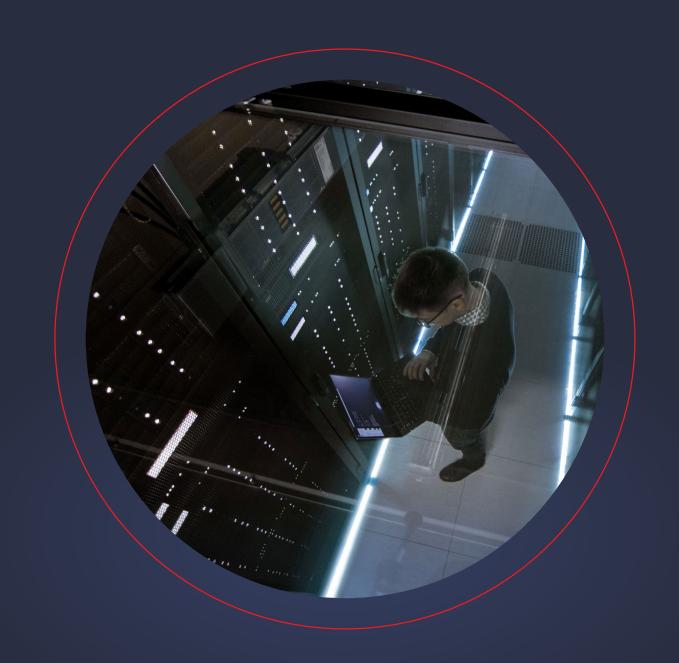
Developers Like You







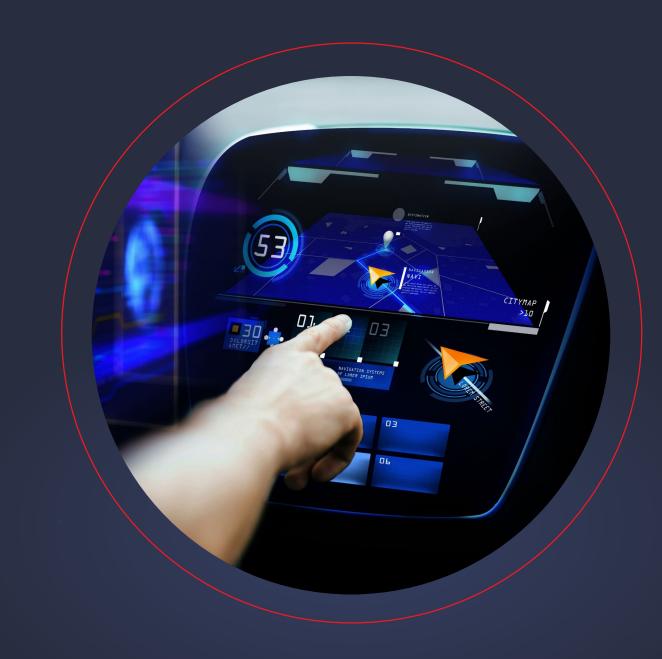
Major Growth Drivers







5G



Automotive

Major Growth Drivers









Data Center

5**G**

Automotiv

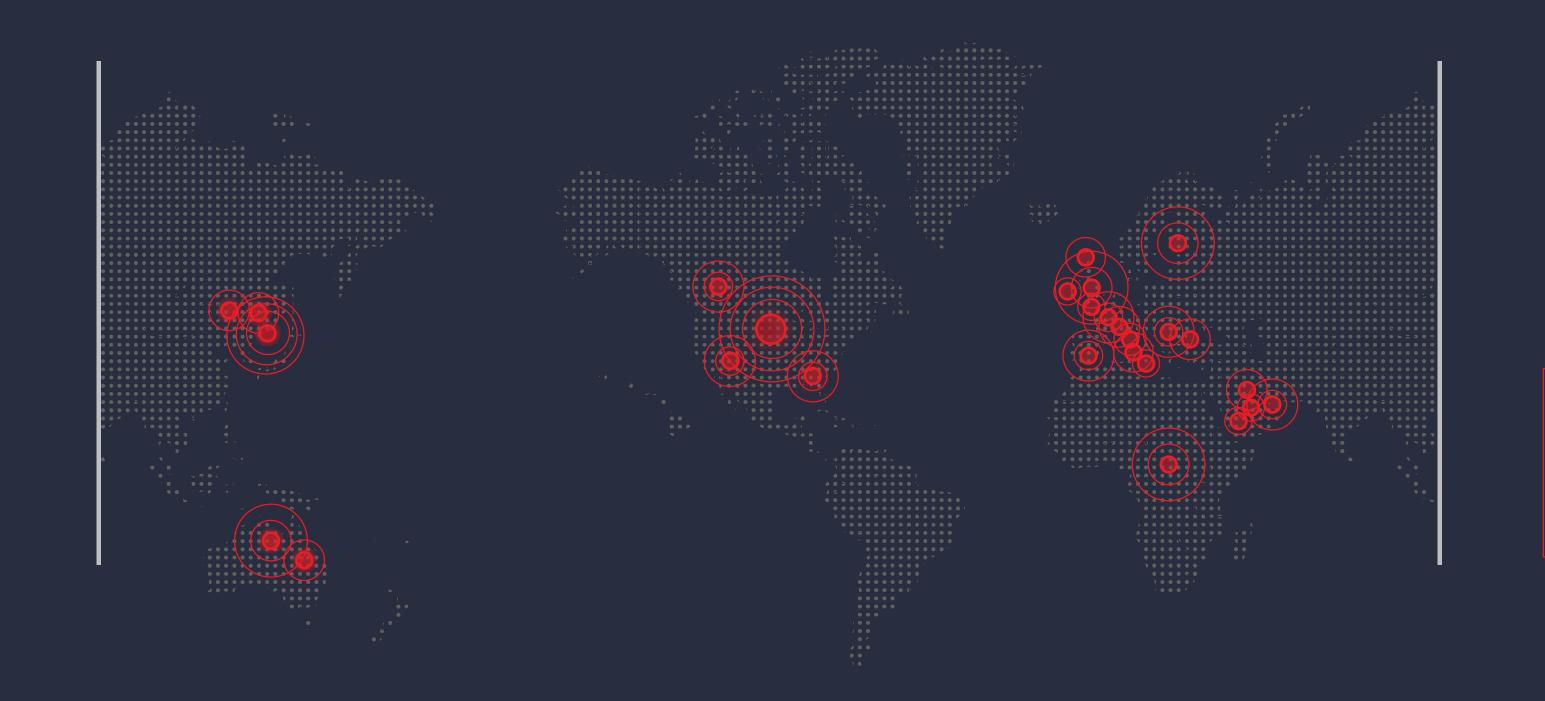
SAMSUNG

Wonil Roh, Ph.D.

VP, Head of Product Strategy, Networks Business, Samsung Electronics

5G is Being Deployed Faster than Many Expected





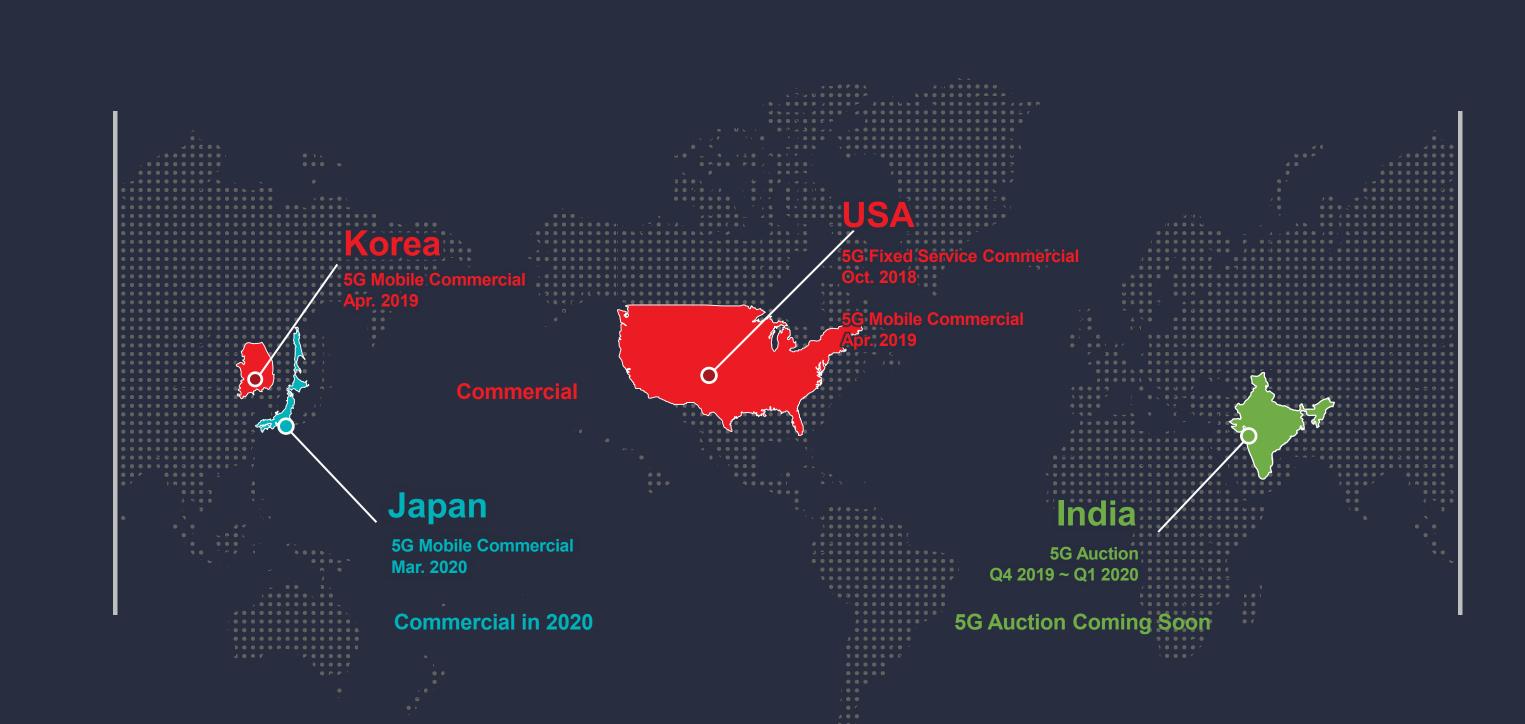


Have Activated One or More 5G Sites within Their Live Commercial Networks

(Source: Evolution from LTE to 5G, GSA, as of Aug. 2019)



USA & Korea Leading the Pack







USA, a Pioneer in 5G

Apr. 2019

5G Mobile Commercial

mmWave

Jun. 2019

5G Mobile Commercial

Mid-Band

Future

Low Band Refarming to 5G

Low-Band

Oct. 2018

5G Fixed Broadband Commercial

mmWave

Samsung, Leading USA into 5G Commercialization

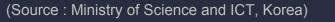
Through Cooperation with Three USA Carriers





Korea, Skyrocketing 5G Subscribers





Korea, The World's Most Extensive 5G Rollout





Samsung, Largest Share of 5G Network Solutions in Korea

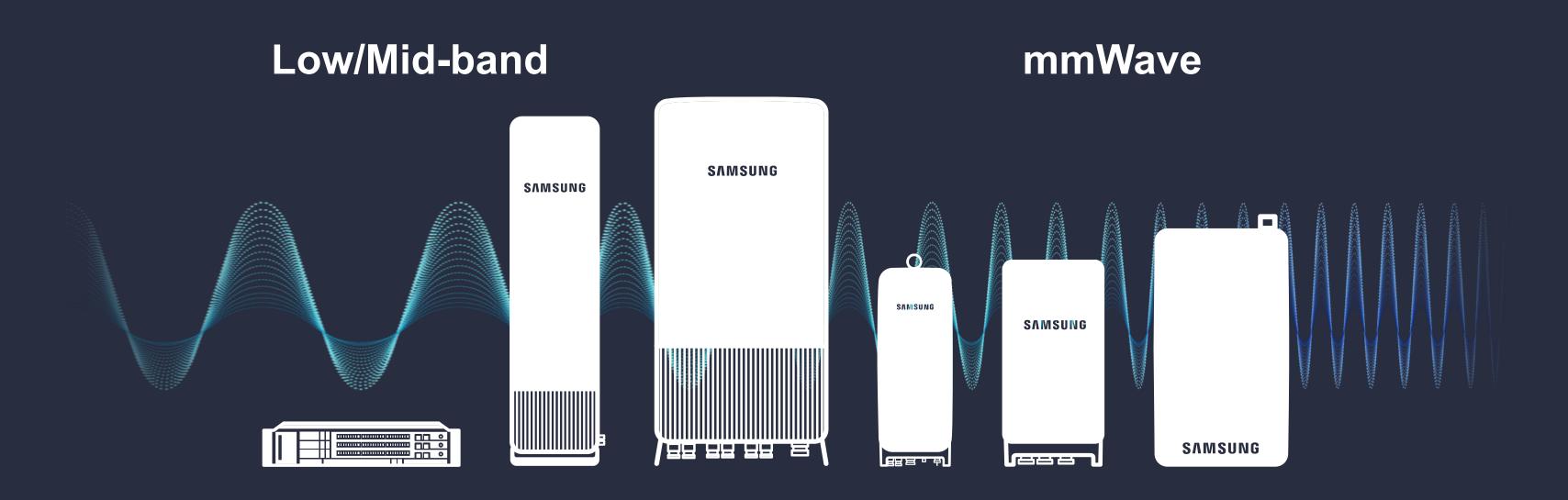
By Meeting Customers Requirements and Speedy Delivery of 5G RAN & Core





5G Collaboration to Success

For New Bands, New Types of Products







FPGA Commercial Development Since September 2017

A State-of-the-Art 5G Through Close Collaboration

Future Cooperation with Versal ACAP

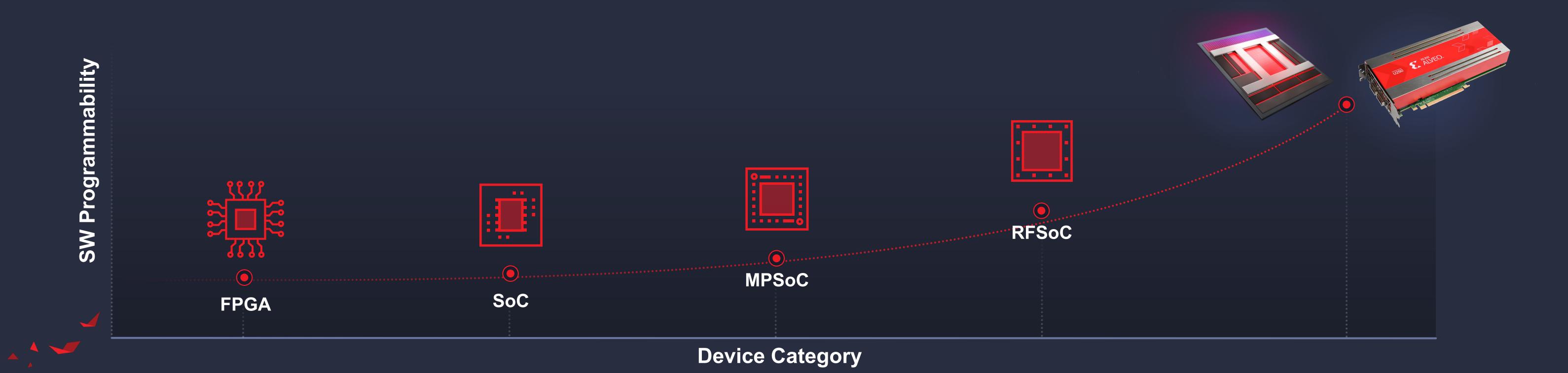




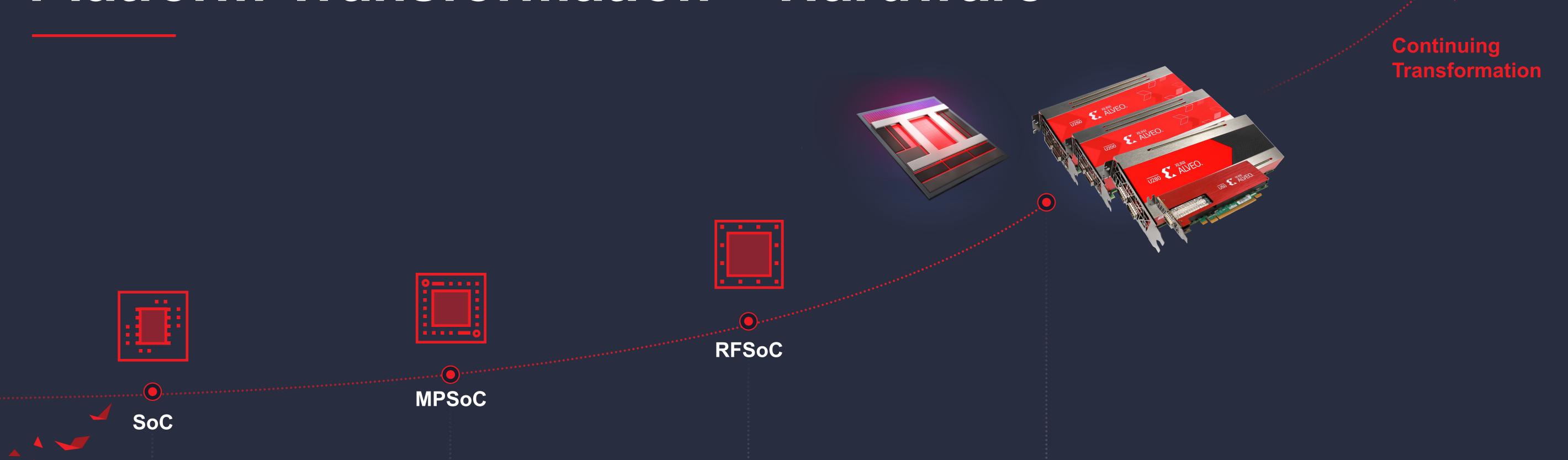


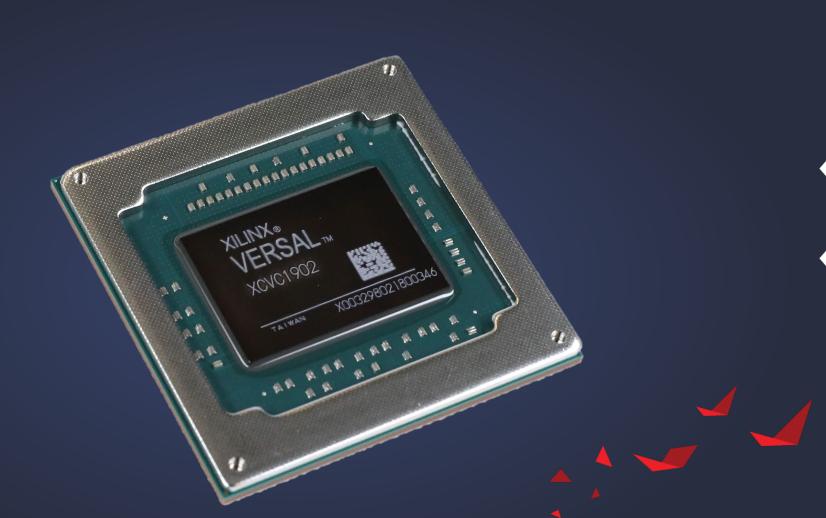
Thank You

Platform Transformation — Hardware



Platform Transformation – Hardware







XILINX® VERSAL™



Major Growth Drivers







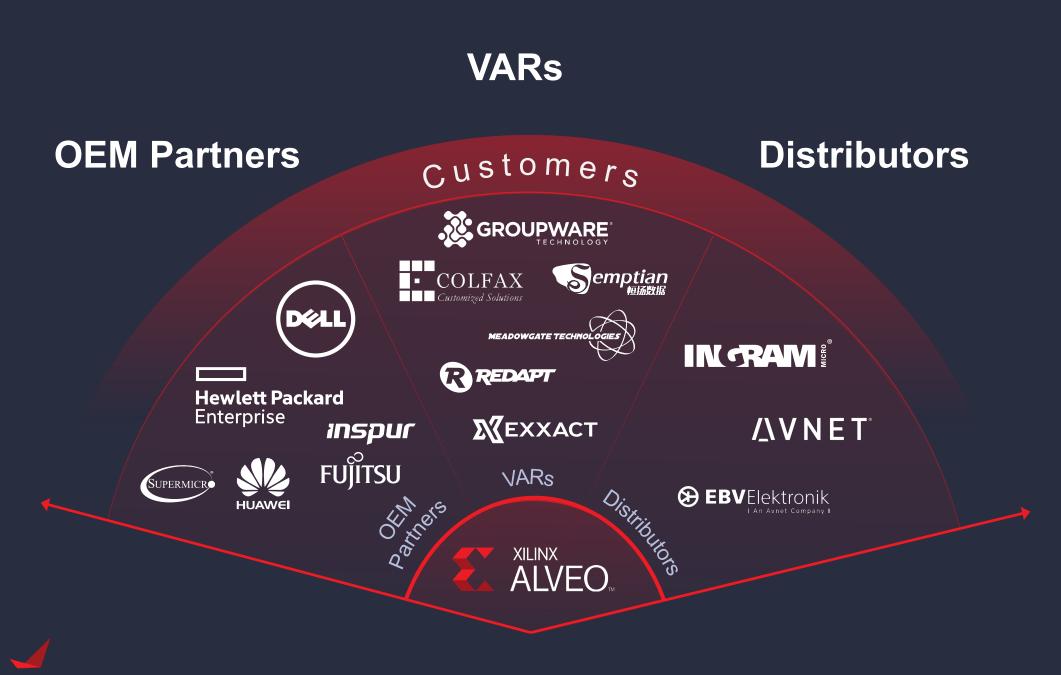




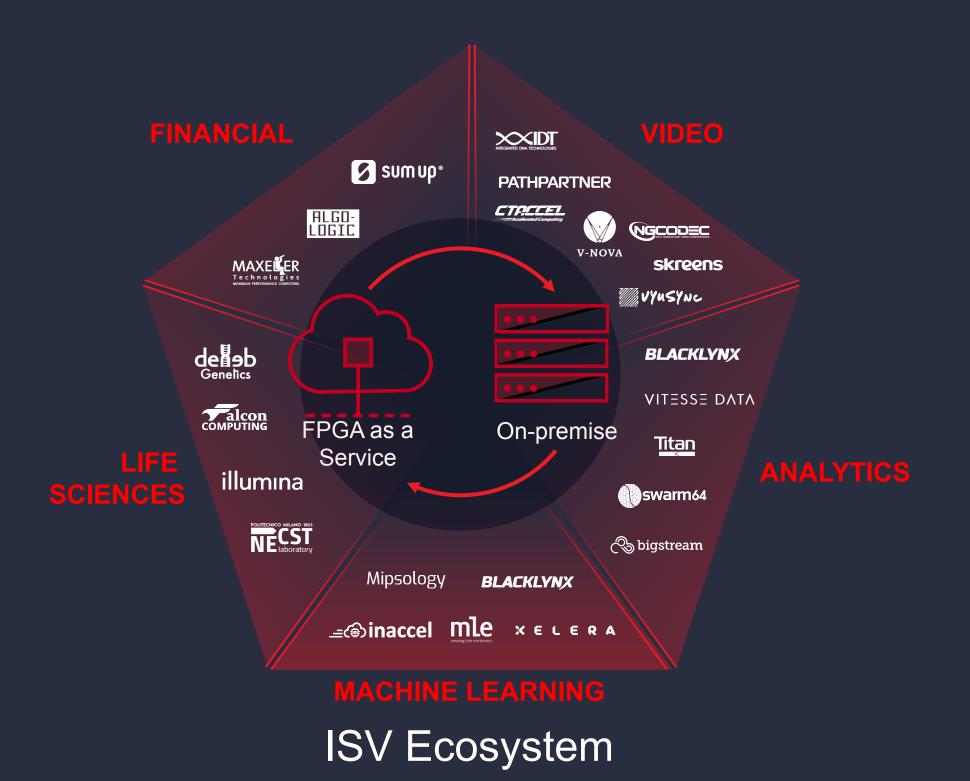




Data Center Ecosystem Growth



Alveo Ecosystem



5815

Companies and Academia

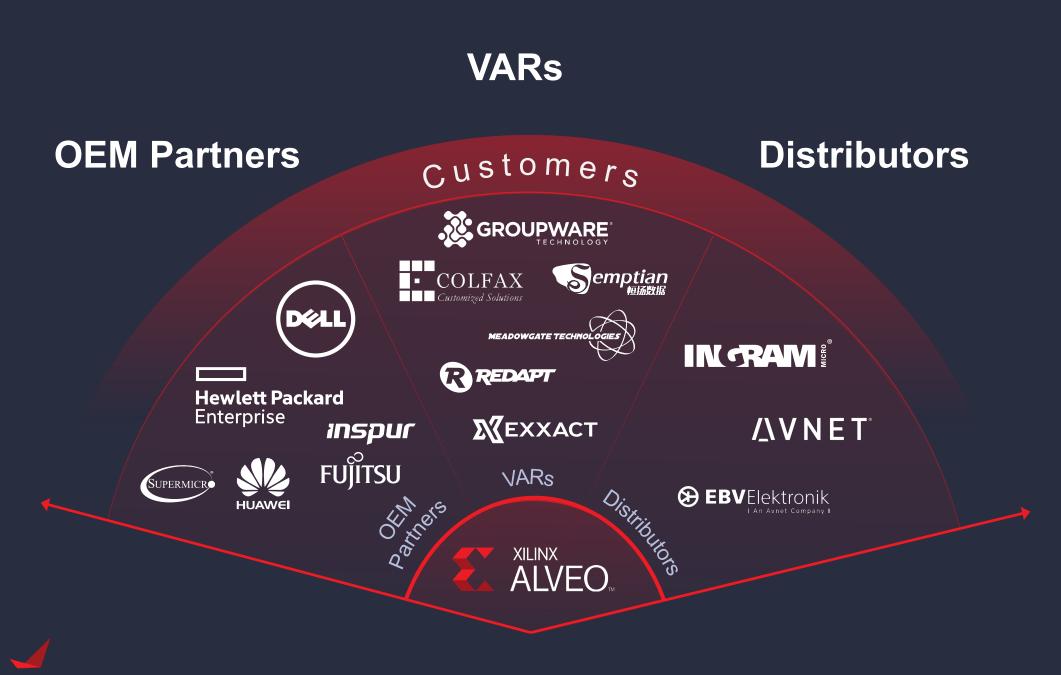
725

Accelerator Program

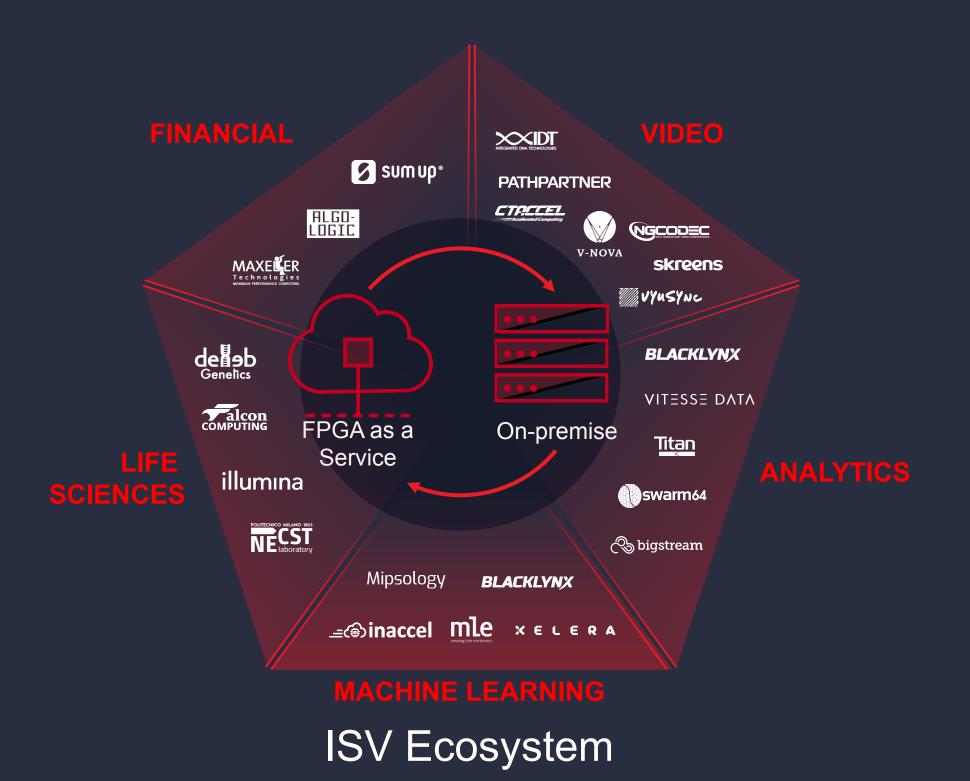
85

Published Apps

Data Center Ecosystem Growth



Alveo Ecosystem



5815

Companies and Academia

725

Accelerator Program

85

Published Apps



Gadi Hutt

Senior Director, Amazon Web Services

Vin Sharma

Head of Engineering, Amazon Web Services

AWS Recognized as a Cloud Leader for the 9th Consecutive Year

Gartner Magic Quadrant for Cloud Infrastructure as a Service, Worldwide





AWS FPGAs at a Global Scale



EC2 F1 is Available in Canada (Central) AWS Region



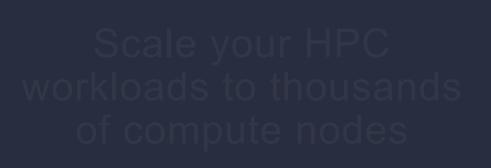
AstraZeneca are Raising the Bar Running Their Genome Sequencing Pipeline on AWS

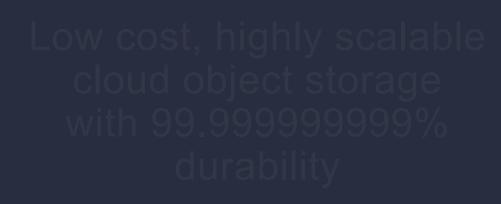




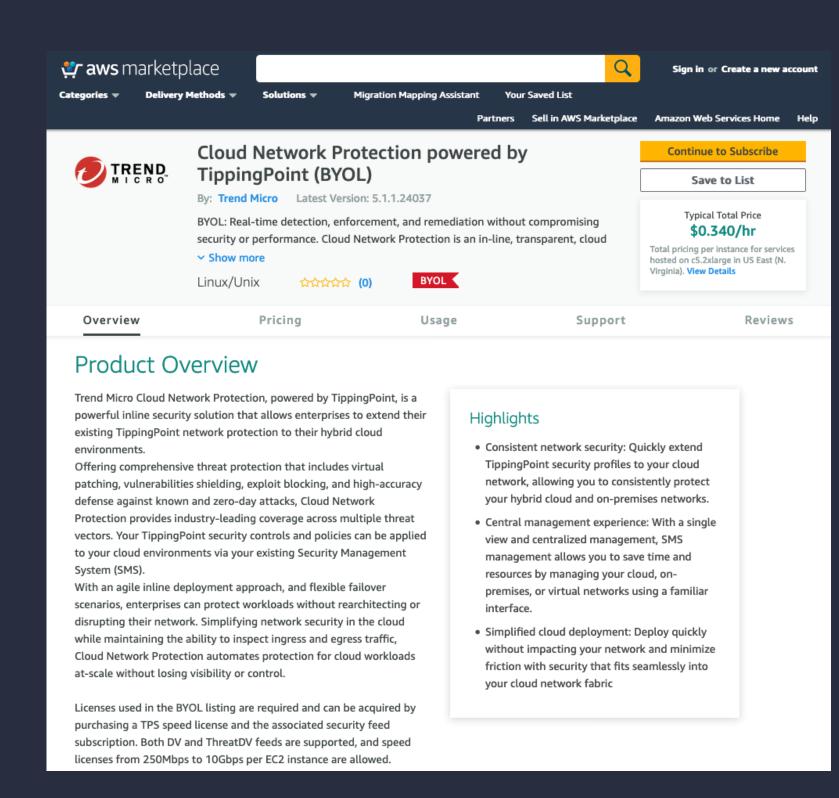
100,000 Genomes in 100 hours 2 Million Genomes planned





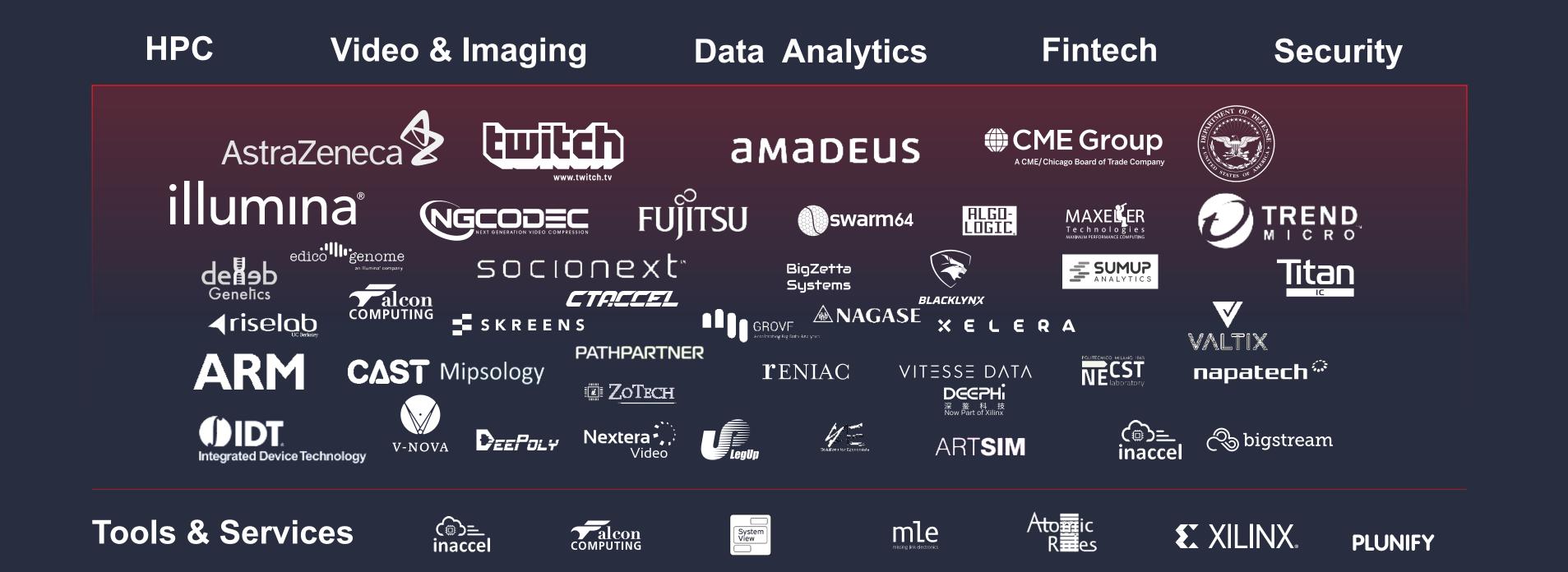


Trend Micro Network Security Powered by EC2 F1



- EC2 F1 Enables Consistent Network Security
- Predictable Network & Application Performance
- Unparalleled Efficiency & Operational Simplicity

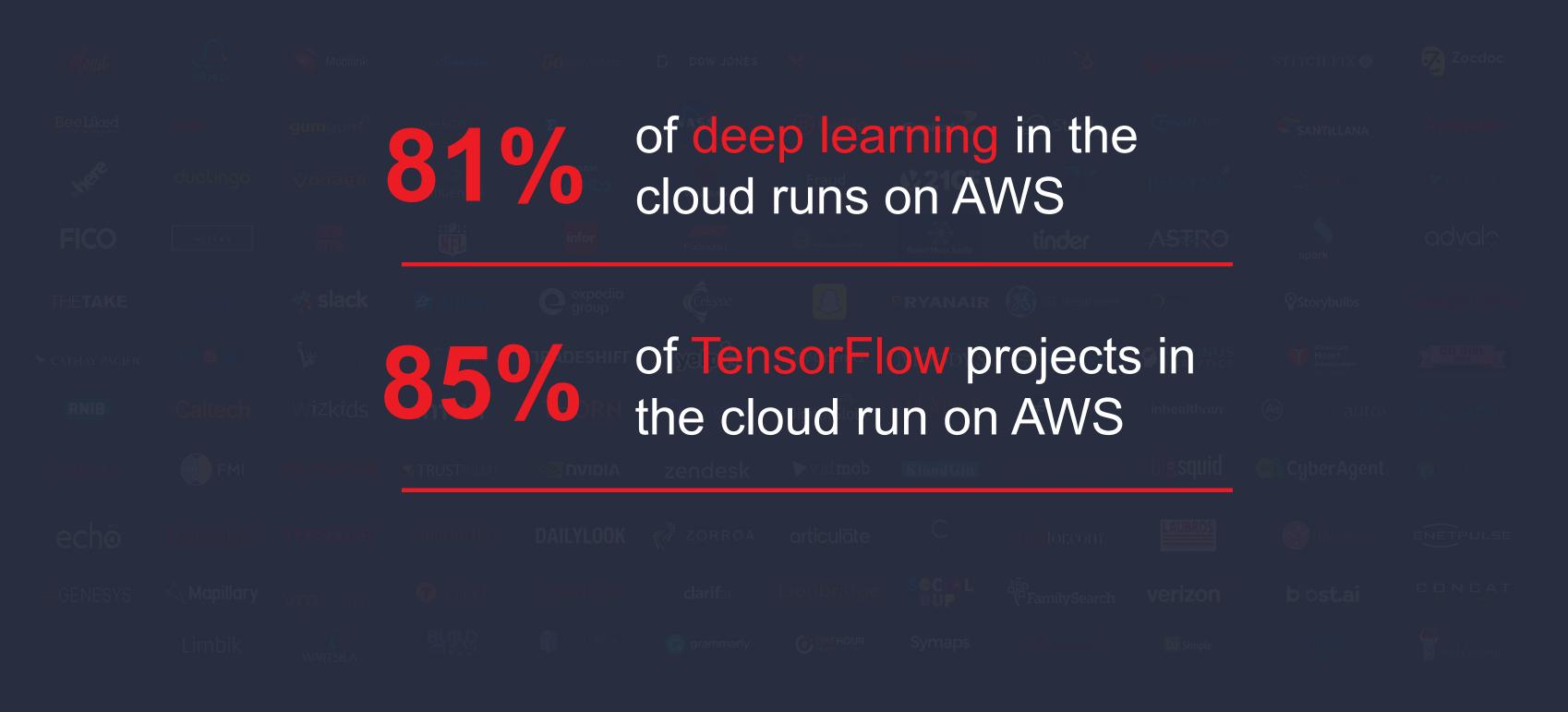
AWS Hosts the Largest Amount Of FPGA Apps in the Cloud





Tens of Thousands of Customers

More Machine Learning Happens on AWS Than Anywhere Else





Amazon SageMaker

Build, Train, & Deploy ML Models at Scale

Pre-Built
Notebooks
for Common
Problems

Collect and prepare training data

Built-In, High Performance Algorithms

Choose and optimize your ML algorithm

One-Click
Training on the
Highest
Performing
Infrastructure

Set up and manage environments for training

Model Optimization

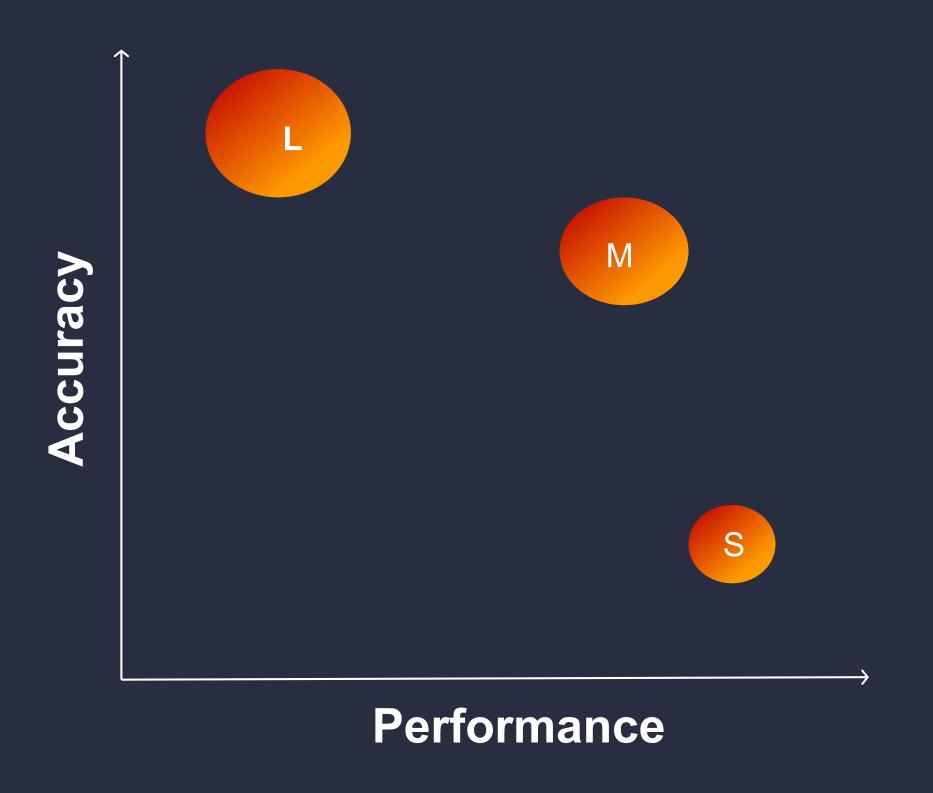


Train and Tune ML Models

Deploy models in production



Problem: Not All Models are Skinny

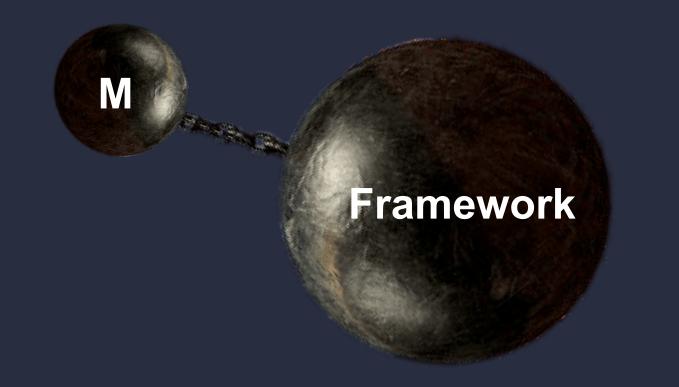


Models that are Accurate Tend to be Big and Slow



Problem: Not All Models are Liberated

Build, Train, & Deploy ML Models at Scale

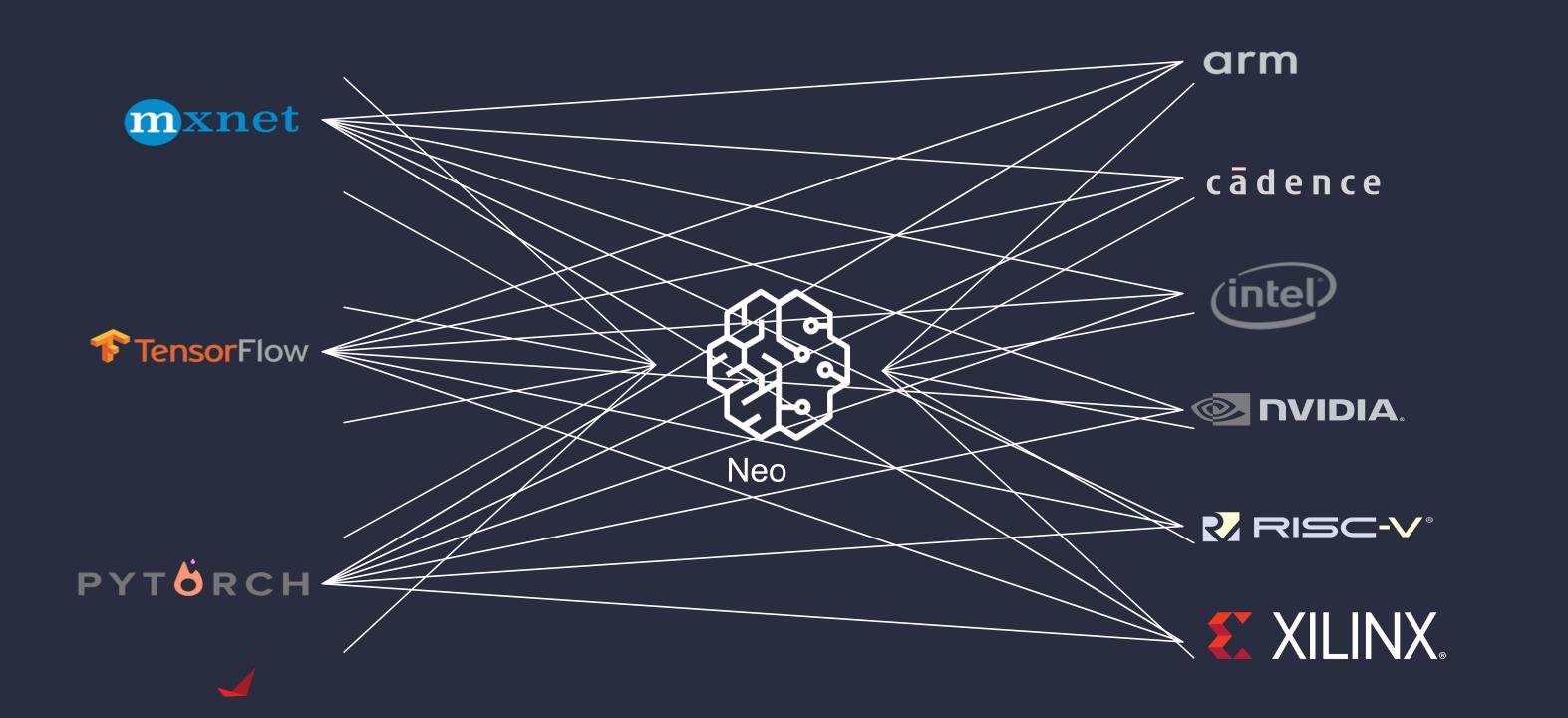


Models are chained to the framework in which they were trained

| Framework | Size |
|------------|---------|
| MXNet | 450 MB |
| TensorFlow | 660 MB |
| PyTorch | 1000 MB |



Problem: Not Every Path is a Catwalk



Model Optimization as a Service

Parses Model Optimizes Graph

Optimizes Tensors Generates Code

Neo Provides

Compact Runtime

Dispatches Model

Partitions Graph

Problem: Not Every Path is a Catwalk

A smart city is a safe and efficient city, that empowers and informs citizens

Kutleng Engineering
Technologies delivers a new
line of security cameras
with AWS IoT Greengrass and
Amazon SageMaker Neo on
Xilinx









License Plate Recognition



Traffic Monitoring



Crime Prevention



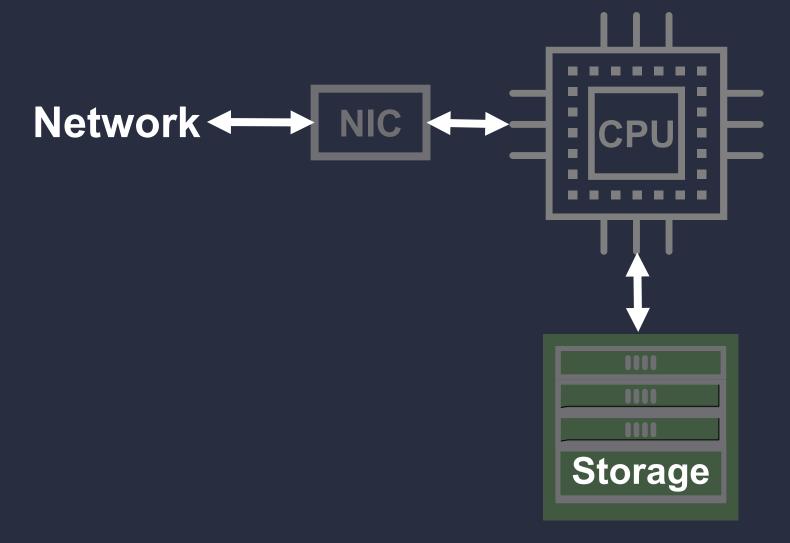
Retail Analytics



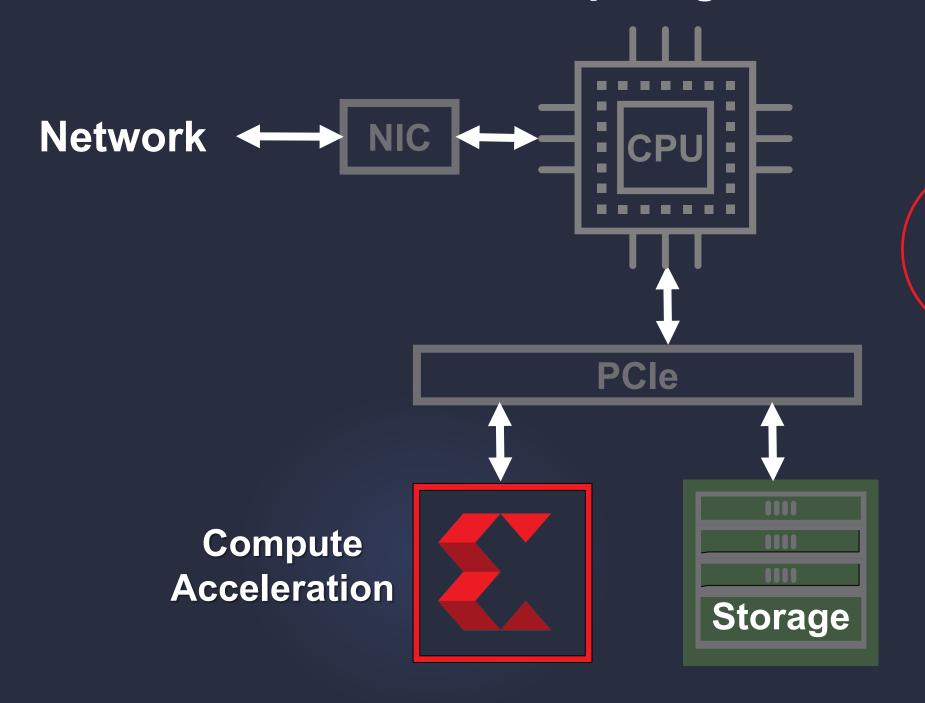
Thank You

Xilinx in Compute Acceleration

Traditional Computing



Xilinx Accelerated Computing





Video Transcoding for VP9 Live Stream



FPGA 30x

Frames per seco

illumına Genomic Data Analytics

1x CPU

FPGA 90x

Performance

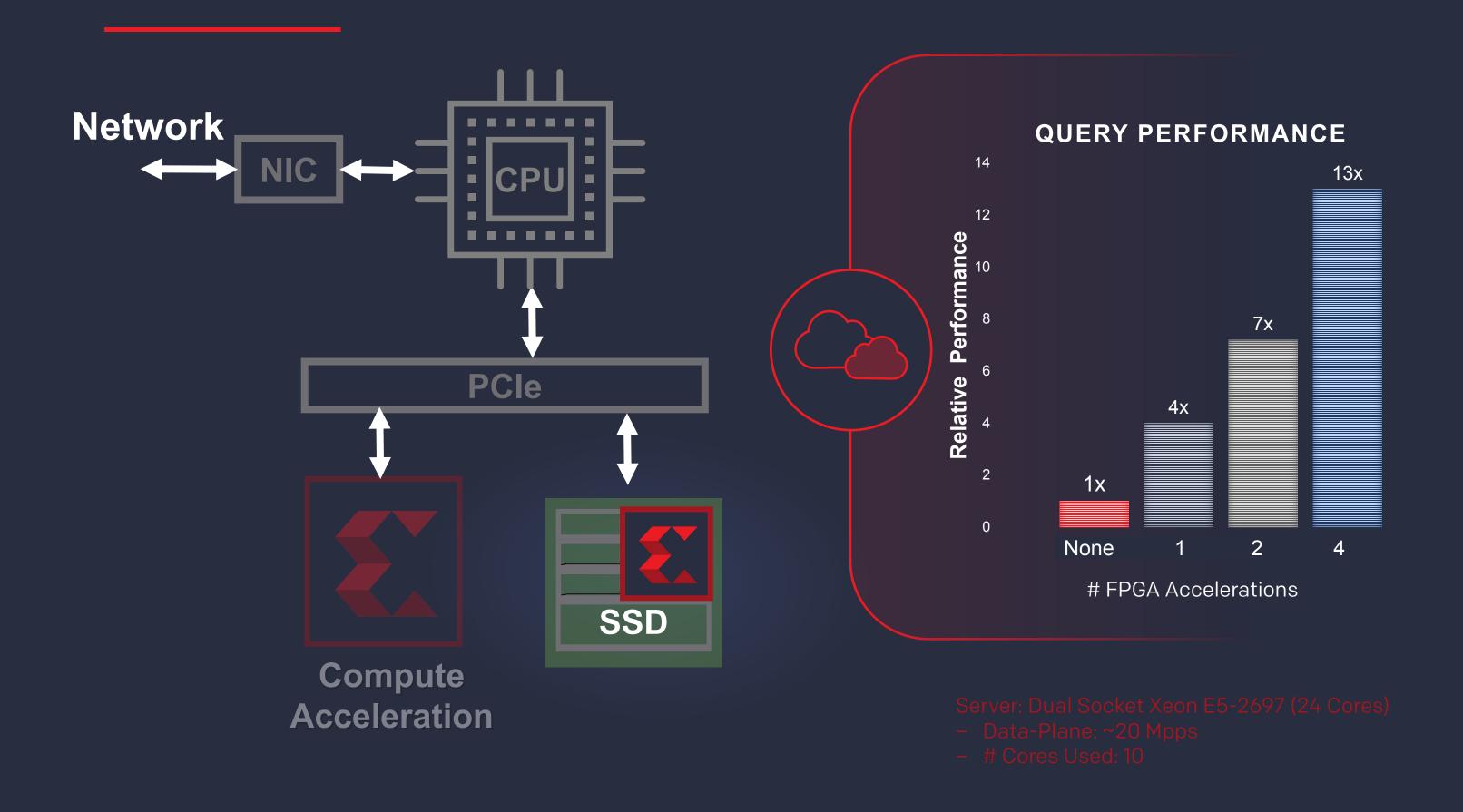
tend Real-time ML Inference

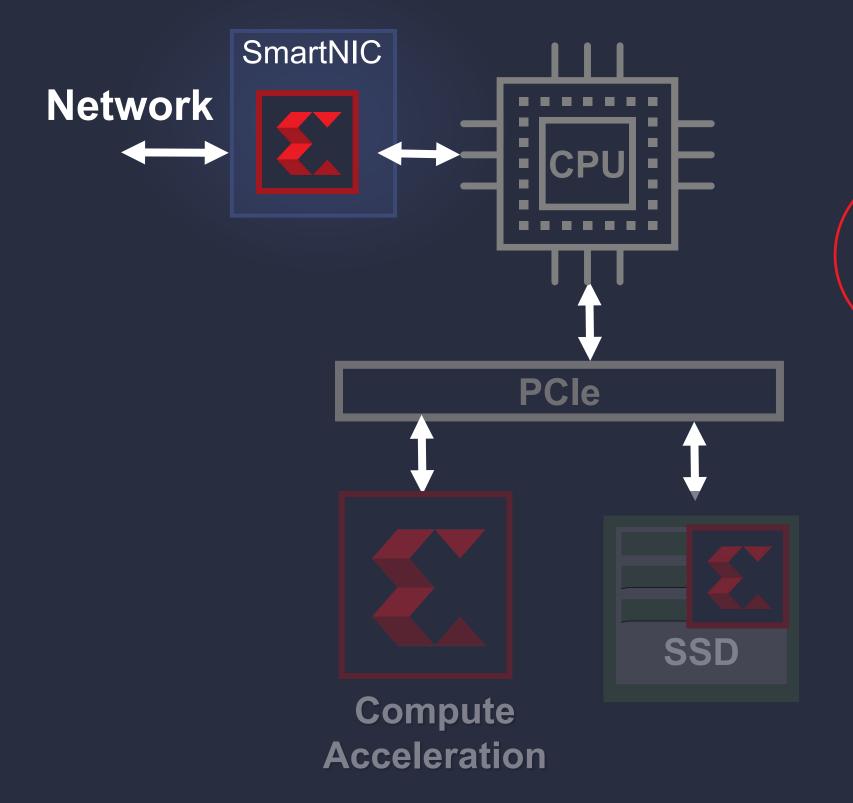


FPGA **100x**

Performance/\$

Xilinx Storage & Network Acceleration





CPU acceleration: 1.5x

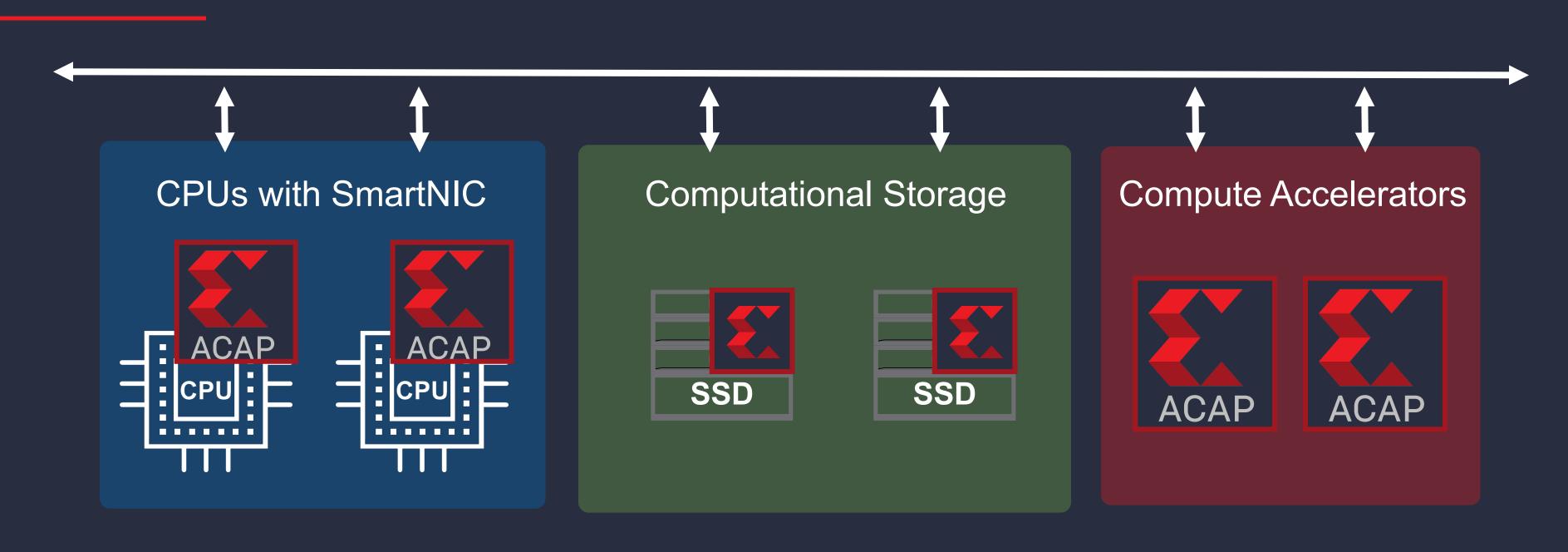
Packet processing throughput: 3-6X

rver + FPGA (VU9P)

- Data-Plane: ~100 Mpps
- # Cores Used: 2

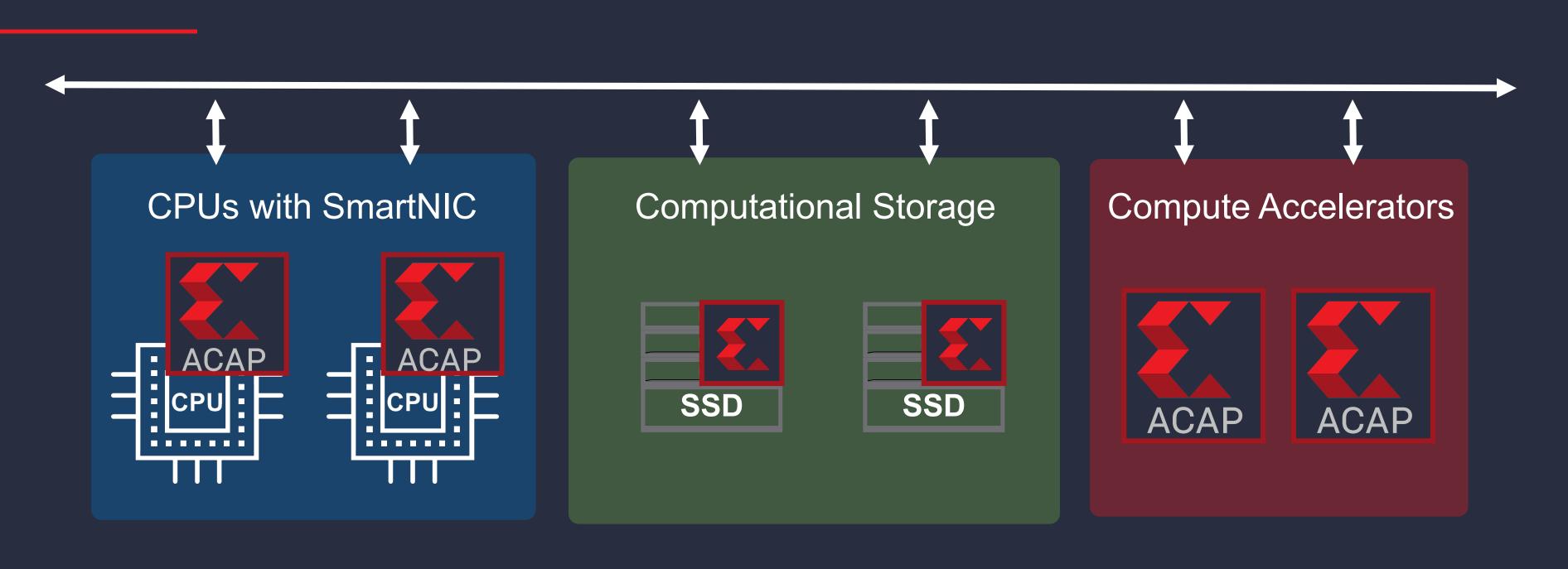
Future of Data Center: Distributed Adaptive Computing

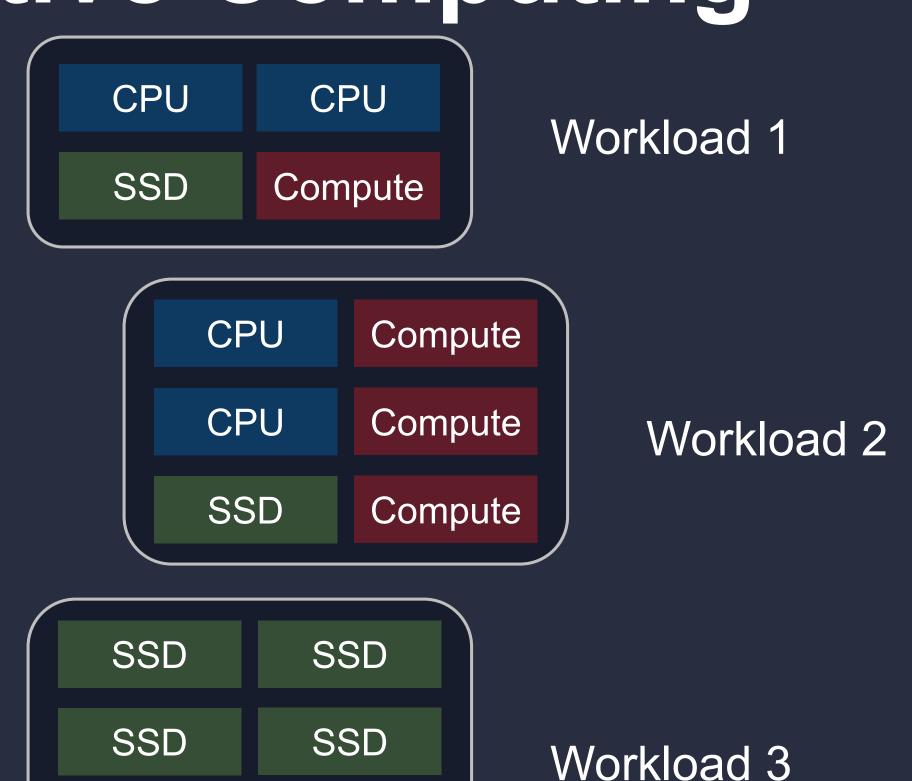
Disaggregated & Composable



Future of Data Center: Distributed Adaptive Computing

Disaggregated & Composable



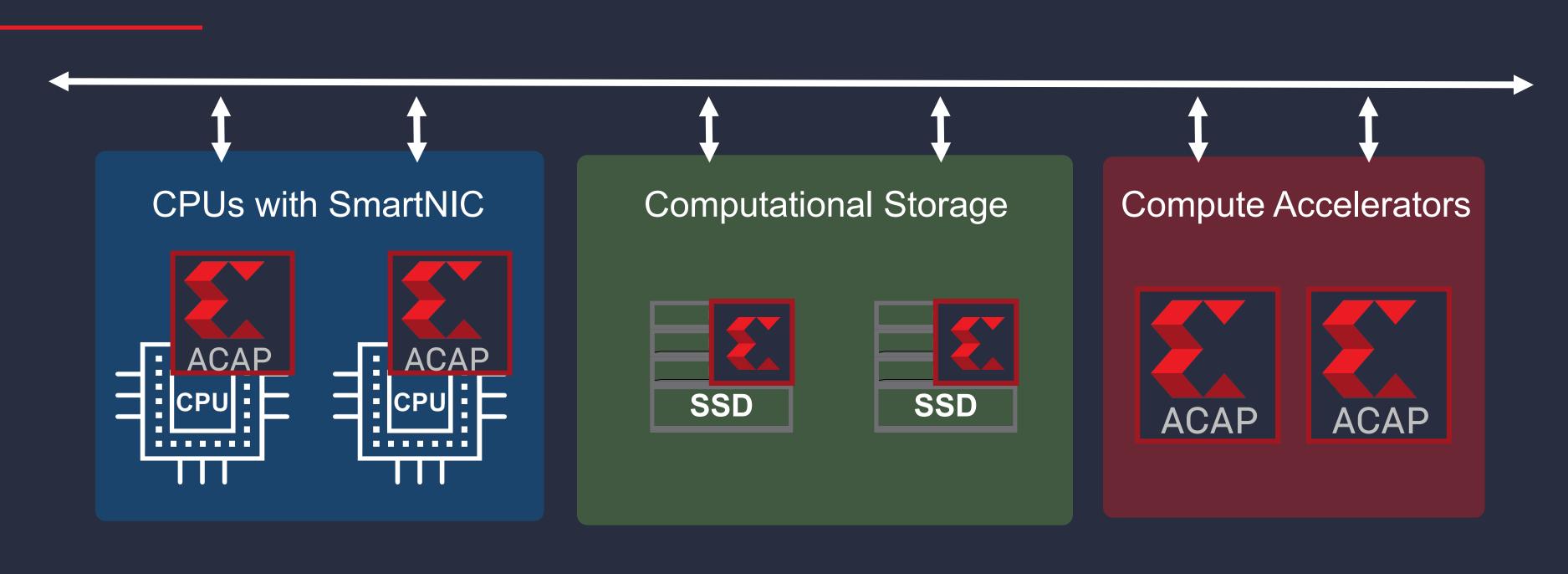


Compute

CPU

Future of Data Center: Distributed Adaptive Computing

Disaggregated & Composable

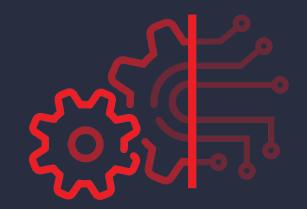




Low Total
Cost of Ownership



High Performance with Low Latency



Customization & Rapid Innovation without New Silicon



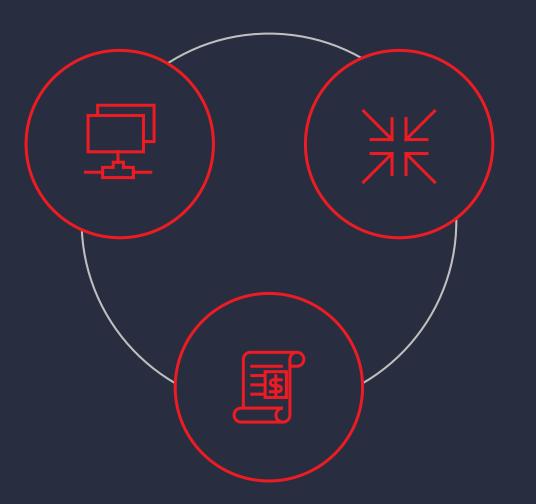
Melur Raghuraman

Distinguished Engineer, Microsoft Azure

Microsoft Azure Million Miles of Fiber Internet Exchange **Terrestrial Network**



Azure Specialized Infrastructure





High-performance VMs
Tightly Coupled
Parallel Jobs with Infiniband



Accelerator-Enabled VMs

NV—Graphic-based applications

NC—GPU Accelerated Compute

ND—Deep Learning

NP—General Purpose FPGA



Cray in Azure

Managed, Custom Bare-metal HPC or Supercomputing

On the Azure Network



>80,000 IOPs
Premium Storage
Low Latency, High
Throughput Apps

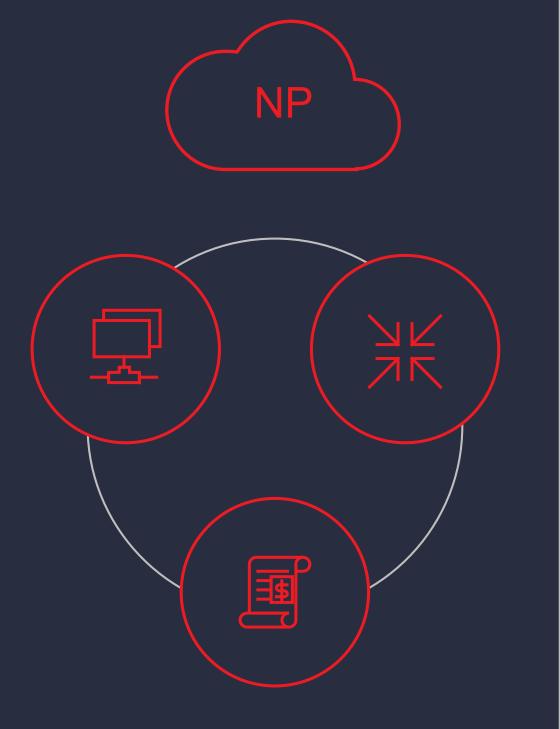


Compute-Optimized VMs
Batch Processing, Monte
Carlo Simulations



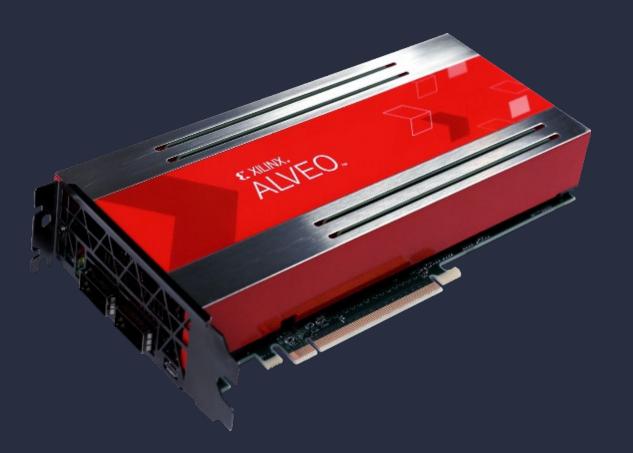
Large Memory VMs
Large Databases

Azure NP VM Family



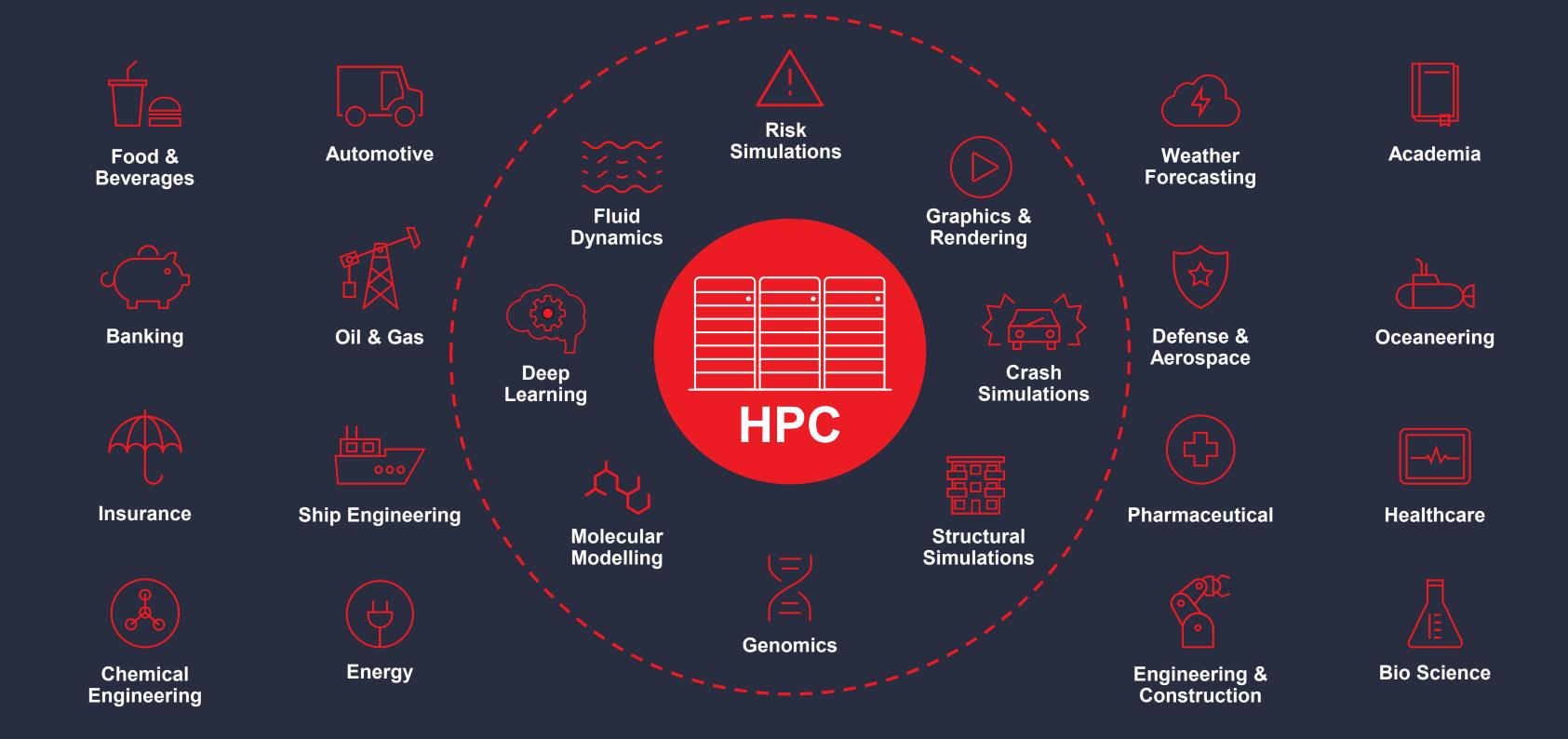
Alveo U250 FPGA-Accelerated VMs coming soon to East US, West US, Southeast Asia, and Western Europe.

| Host VM Specifications | NP10 | NP20 | NP40 |
|-------------------------|--------|--------|--------|
| CPU Cores | 10 | 20 | 40 |
| Local SSD Temp. Storage | 0.7 TB | 1.4 TB | 2.8 TB |
| Host RAM | 168 GB | 336 GB | 672 GB |
| Accelerators (U250s) | 1 | 2 | 4 |





The Wide Reach of Accelerated Applications in Azure





Today's Alveo U250-Ready Products Are Tomorrow's Azure-Ready Solutions





















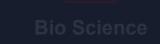






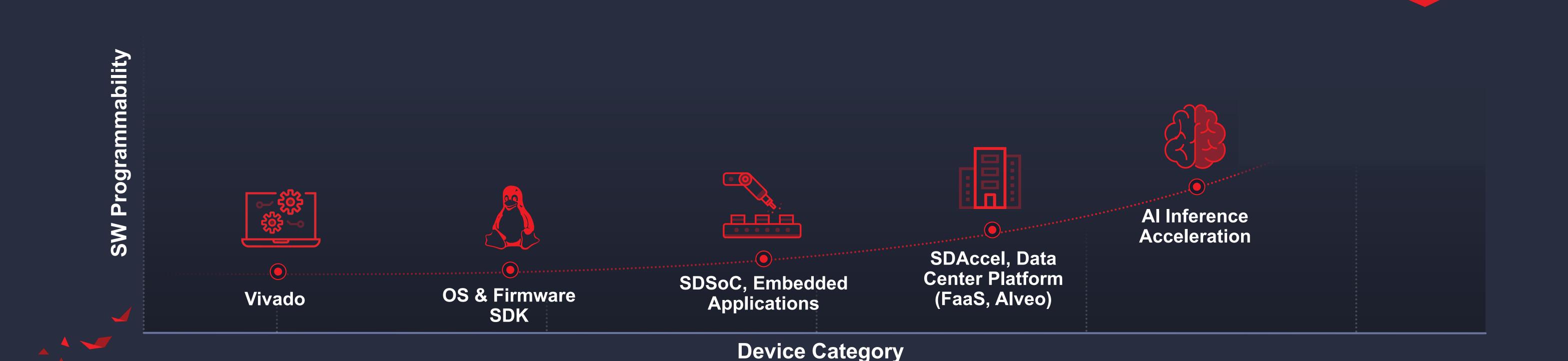




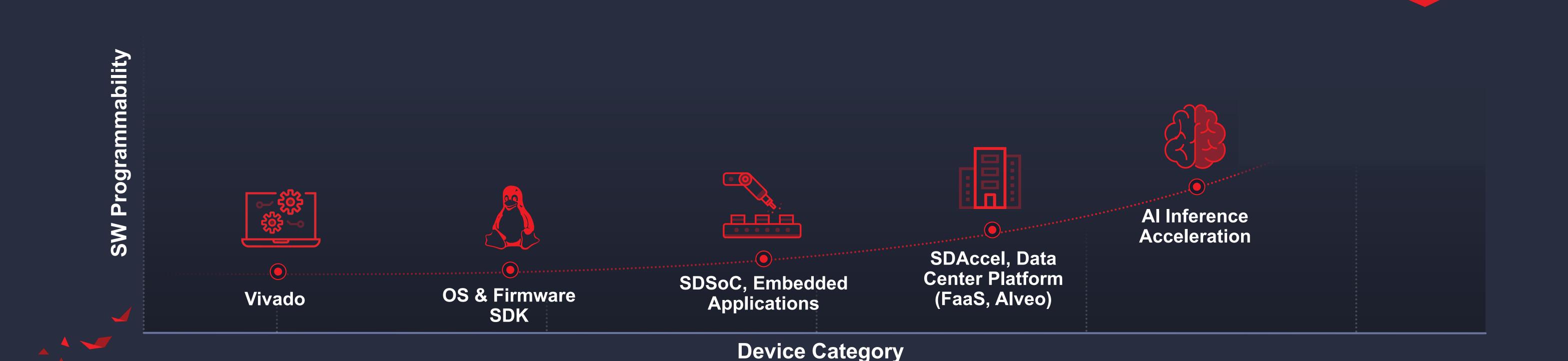


Thank You

Platform Transformation - Software



Platform Transformation - Software



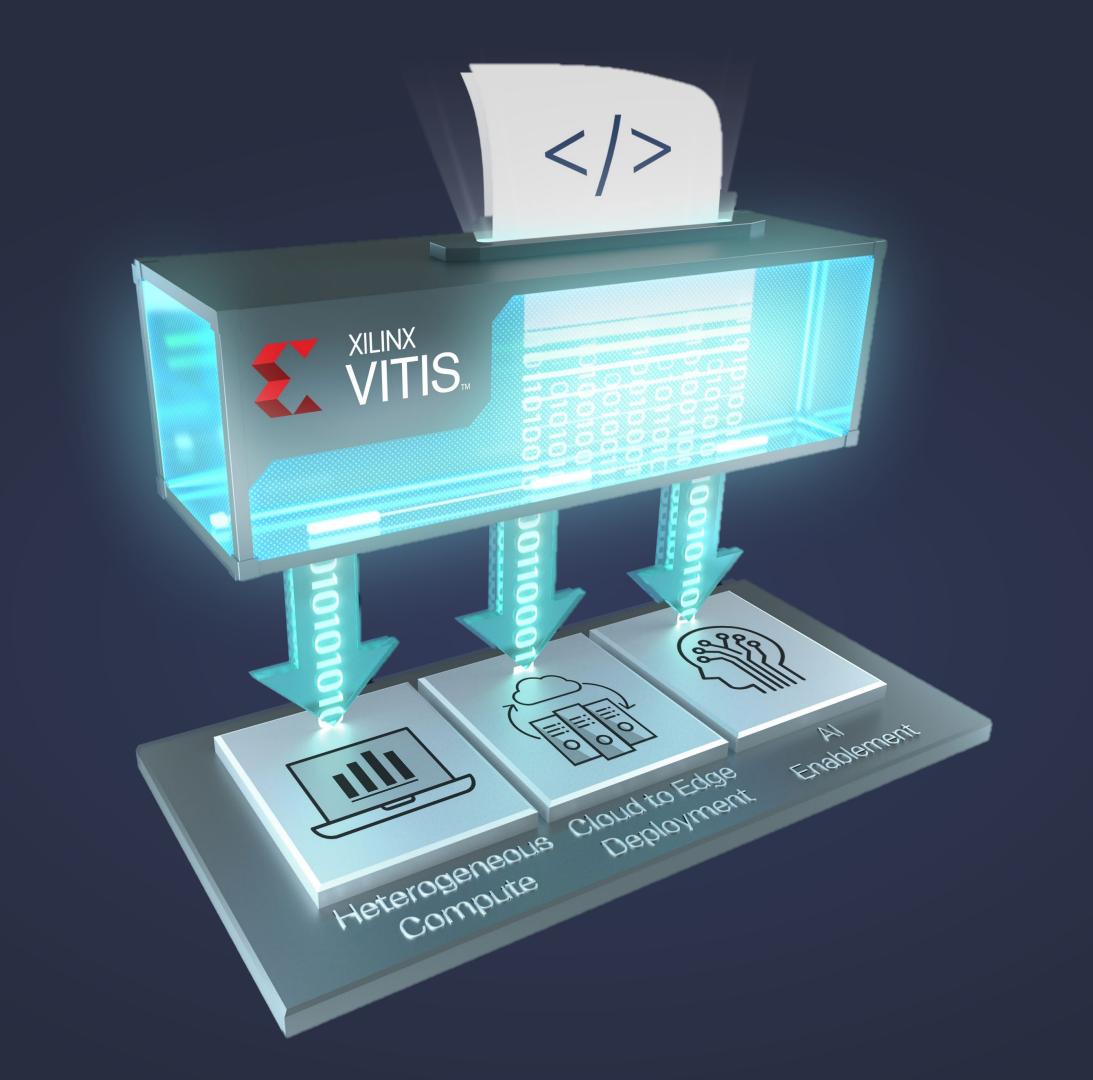
Introducing Vitis Unified Software Platform

Unified Software Platform

Heterogeneous

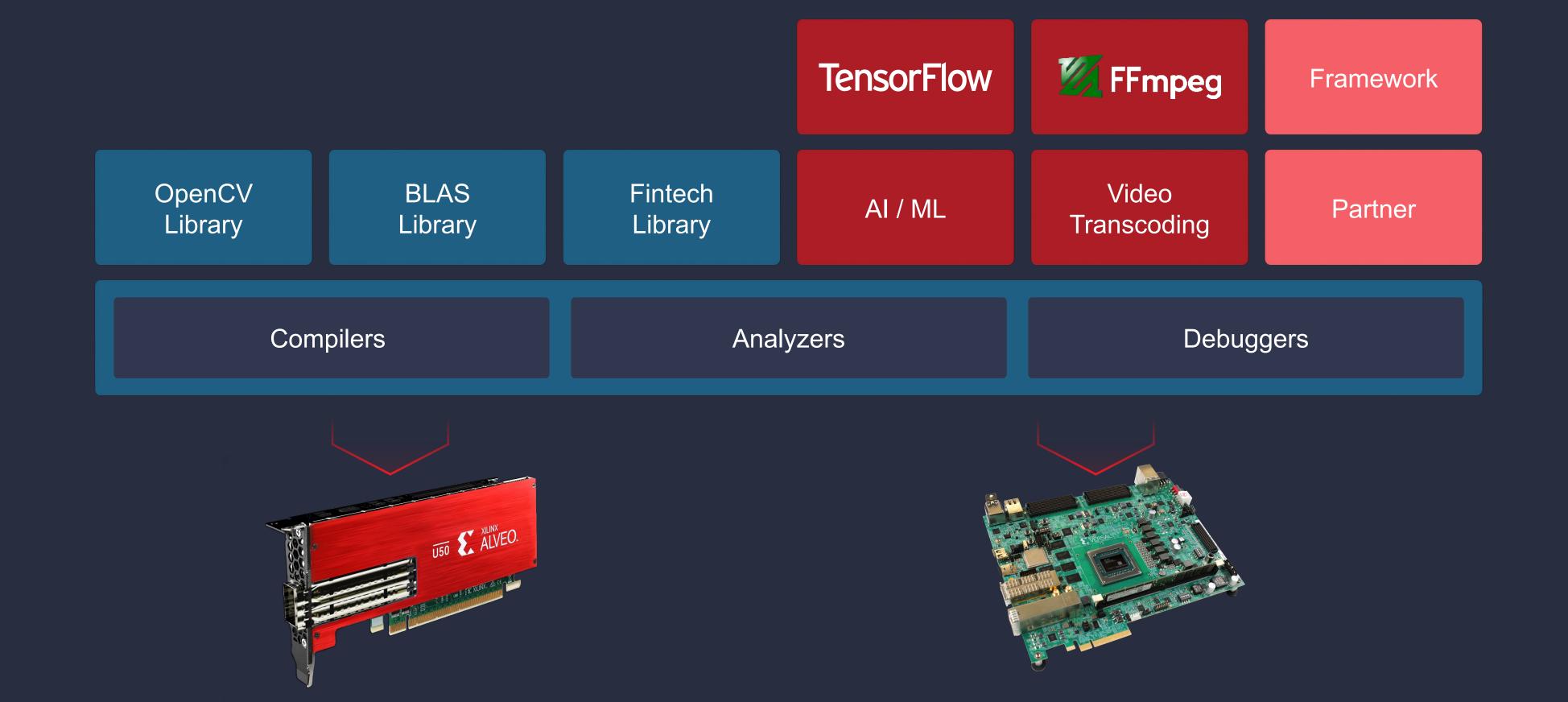
Edge to Cloud

Software & Al





Vitis: Unified Software Platform

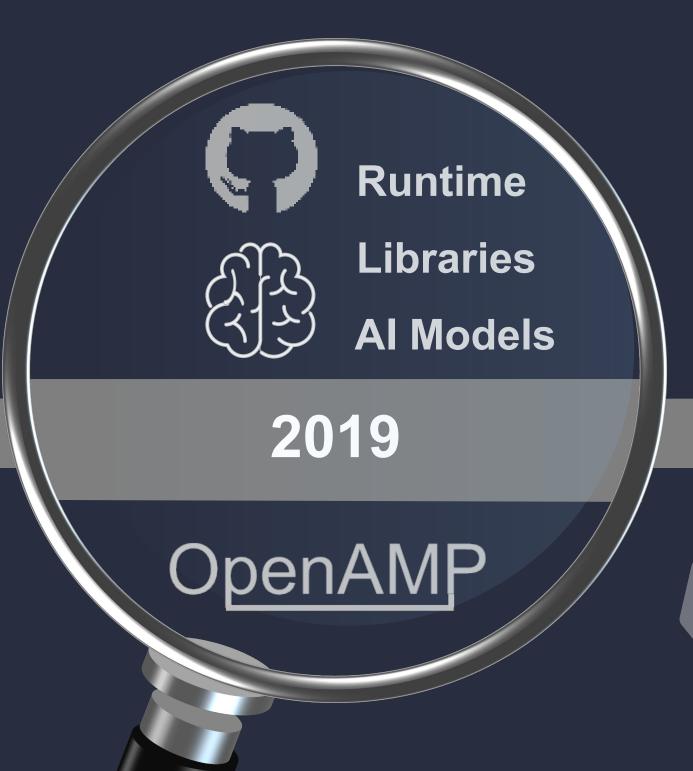


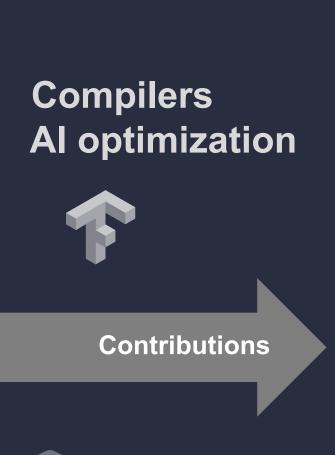
Committed to Open Source

- User Since 2001
- Contributor Since 2007
- Now Core to Xilinx Strategy



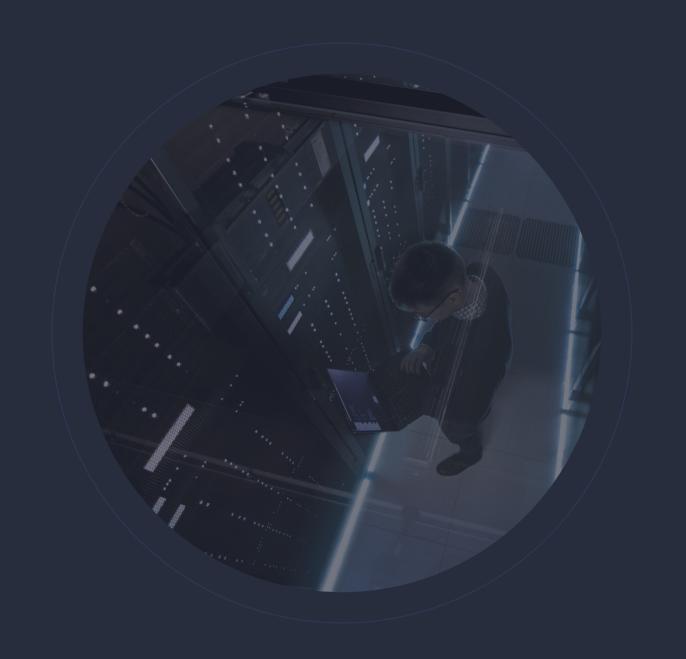
U-Boot



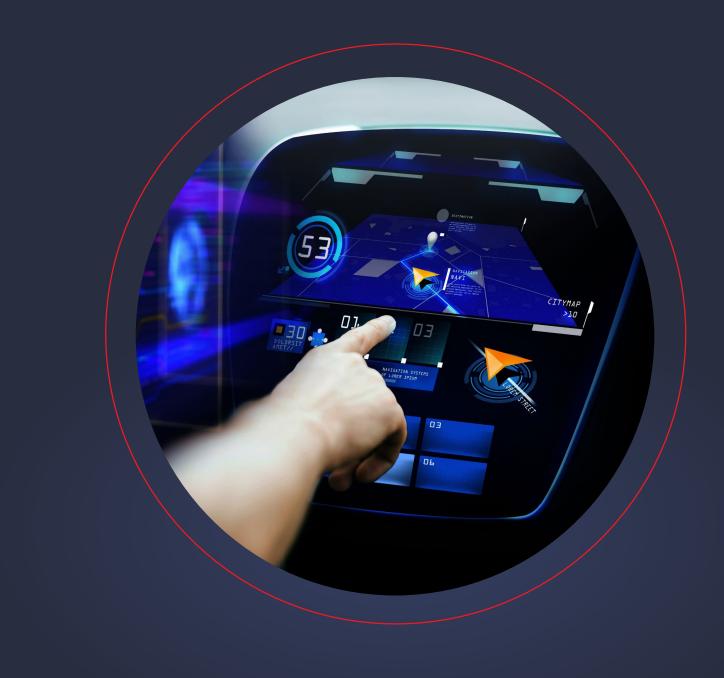




Major Growth Drivers











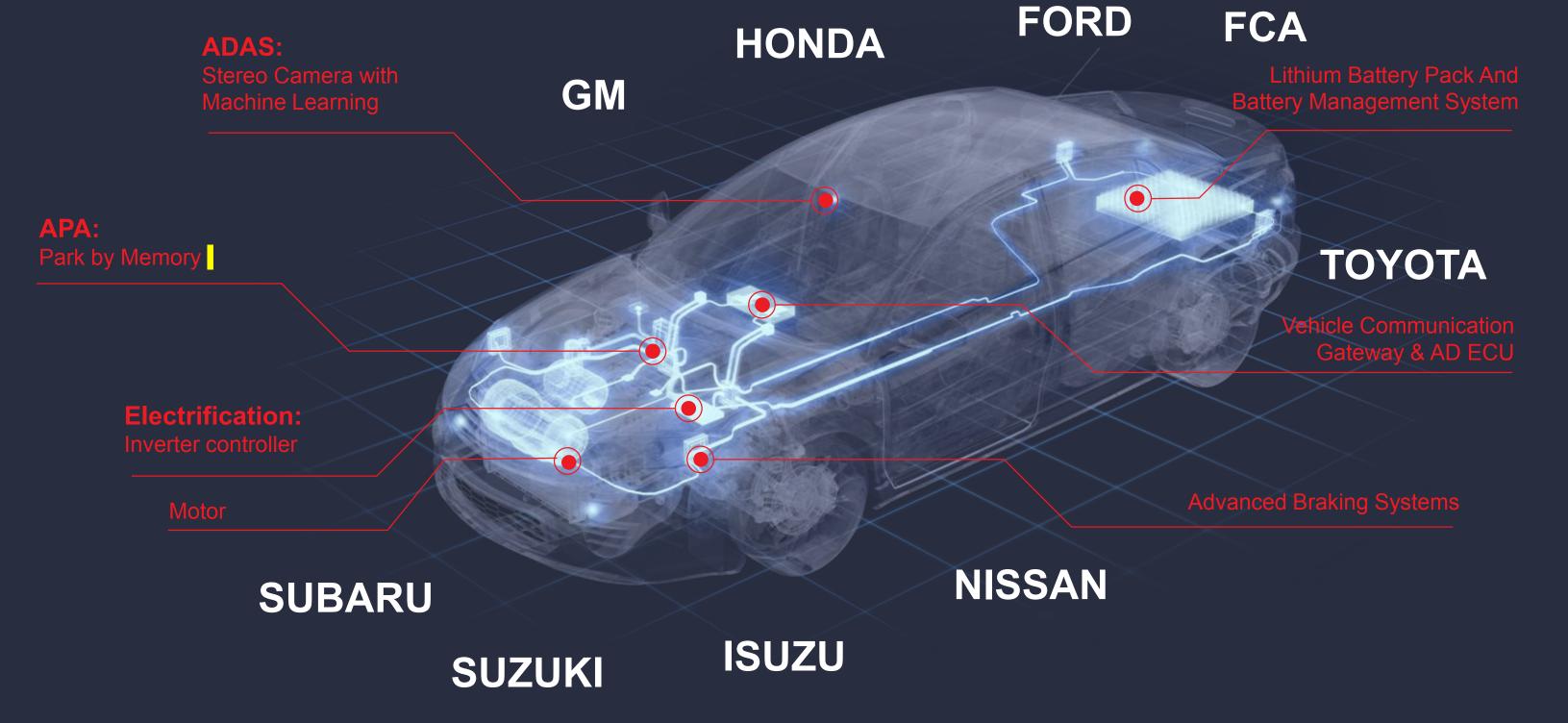
Automotive

HITACHI

Hiroki Uchiyama

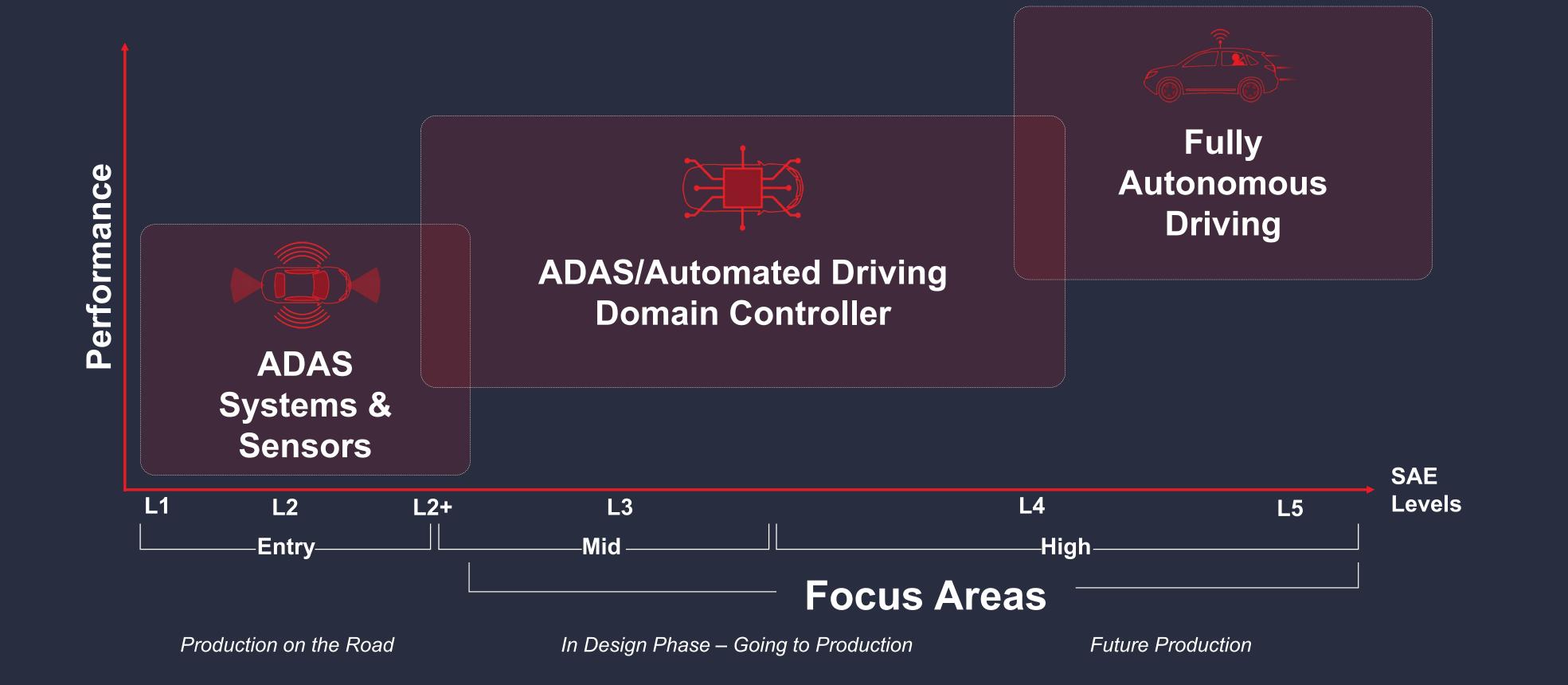
Vice President, Hitachi Automotive Systems of the Americas

Hitachi Products Widely Used By Automotive OEMS



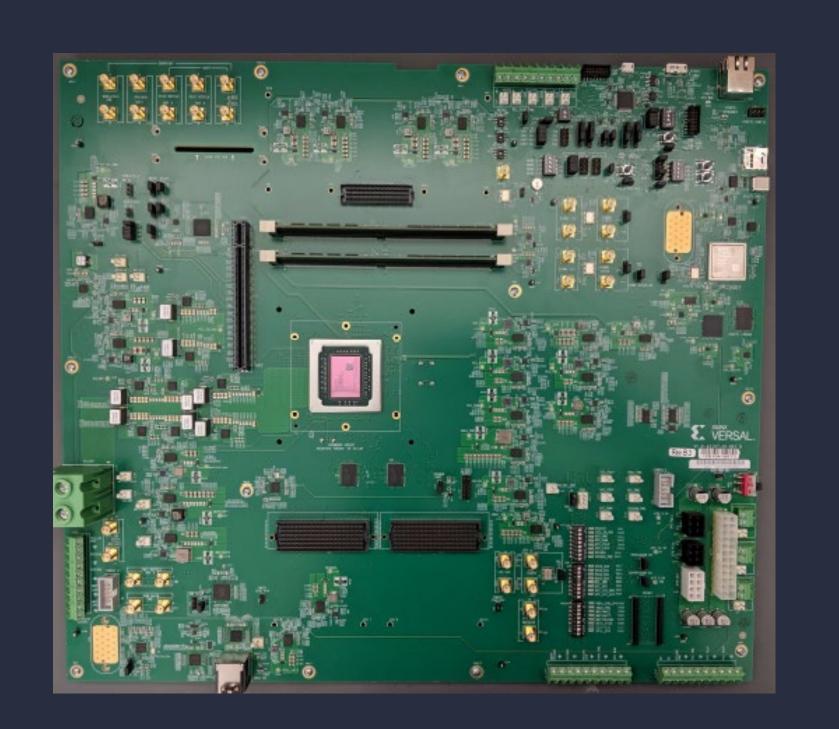


Innovation for Next Generation ADAS / AD Systems



Versal ACAP is the Key to L2+ ADAS

- Tens of TOPS of Al Inference
- Proven Functional Safety
- Adaptable Engine + Al Engine
 - Low Latency End-to-End Processing
- ...and We Received the World's First Versal Development Board!





We Used Vitis... and Here's the Demo

MIPI Camera Input

ISP

Scalar

Deep Learning: SSD-500

Visual Overlay

HDMI DIsplay

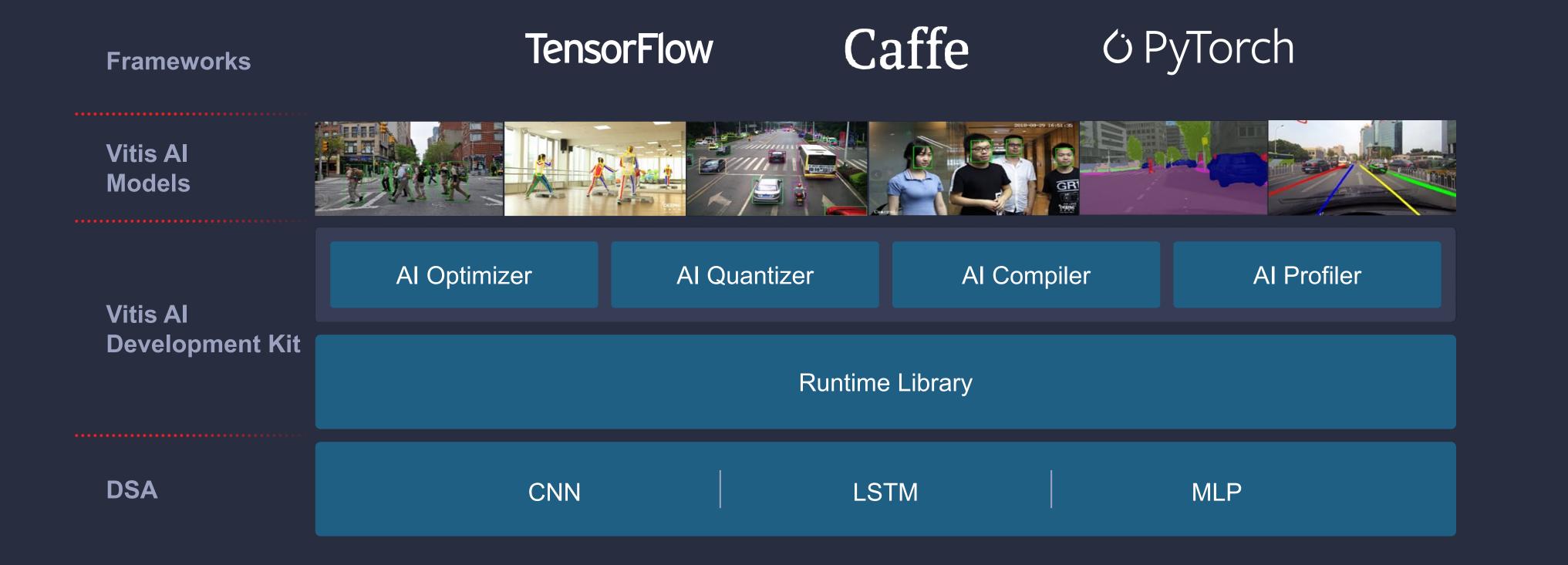






Thank You

Empowering Al Scientists with Vitis Al







James Peng Co-Founder & CEO,

Pony.ai

Pony.ai – A Leading Autonomous Driving (AD) Technology Company







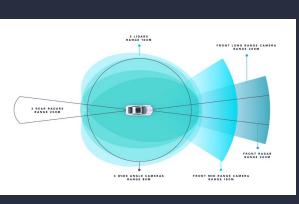








Most diverse urban scenarios



Partnerships with OEMs, Tier 1s, & suppliers



Multiple robo-taxis operations in US & China





US CHINA

play video>

Compute Platform Plays a Major Role in AD Performance

AD compute requirements are not your average compute requirements...

...because an AD vehicle generates massive amounts of data

Generating up to 4TB of data per day per vehicle



Processing over 100 million pixels / second...









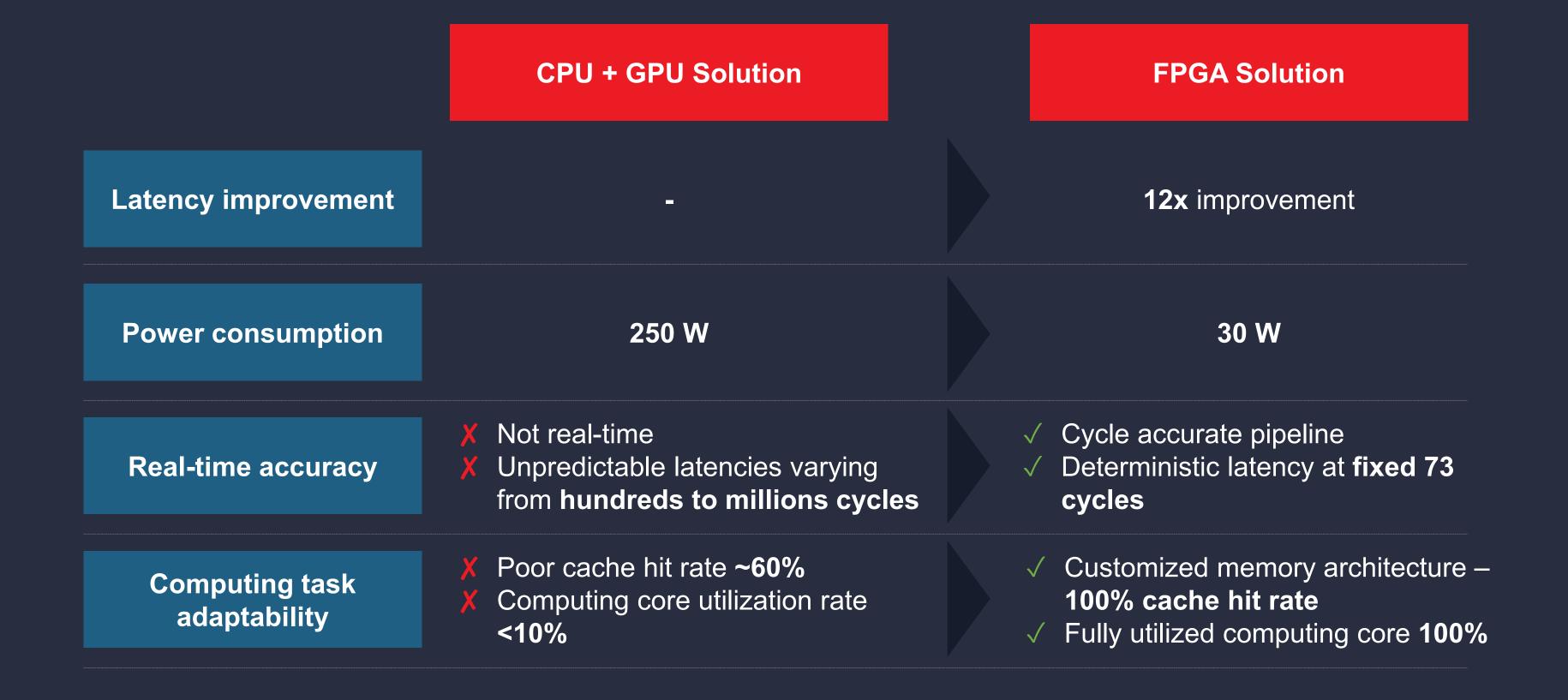
Minimal to no latency



High performance + power efficient



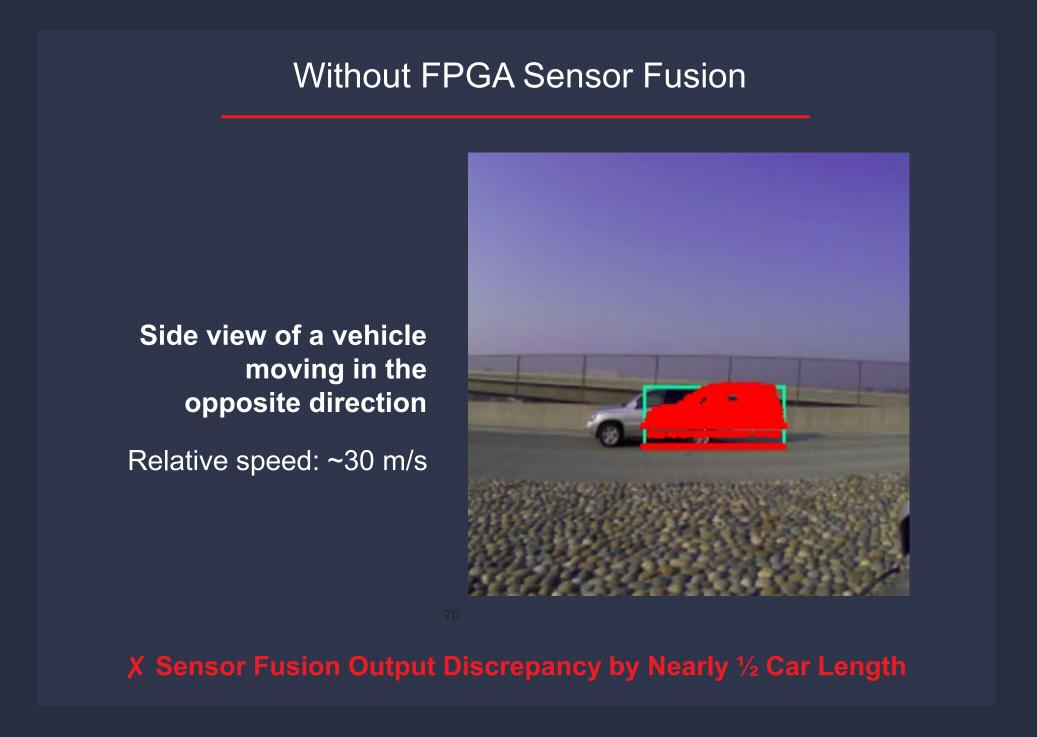
Using Xilinx FPGA has Improved our Compute Efficiency

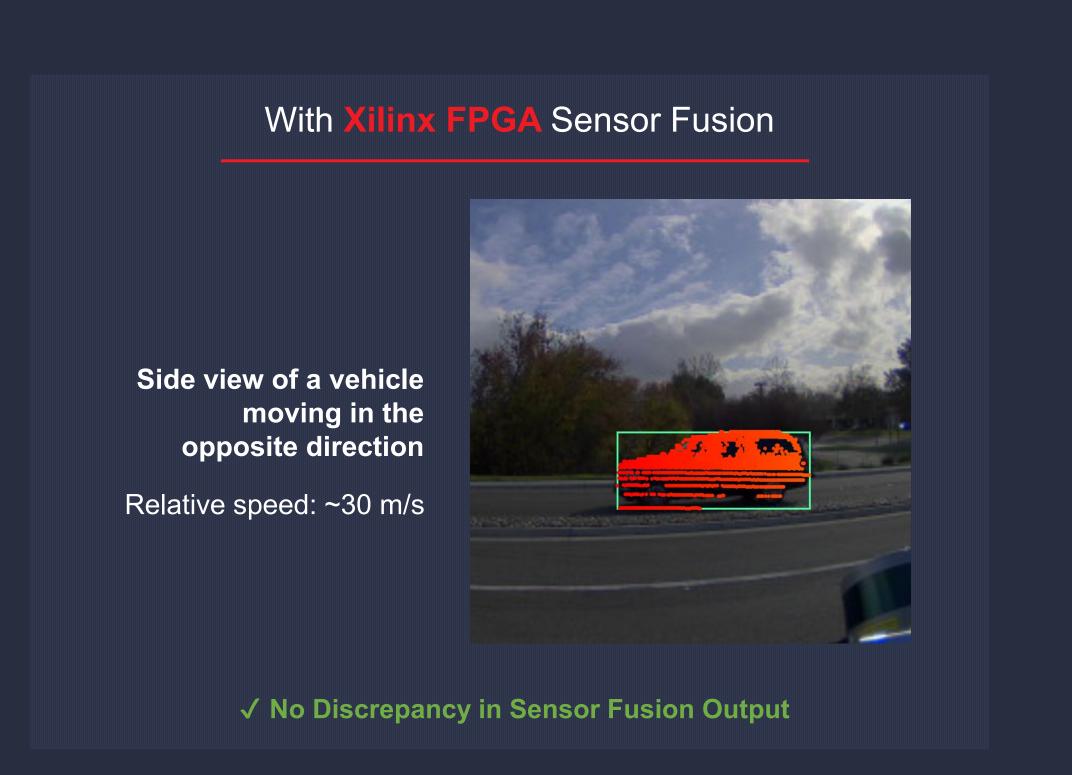




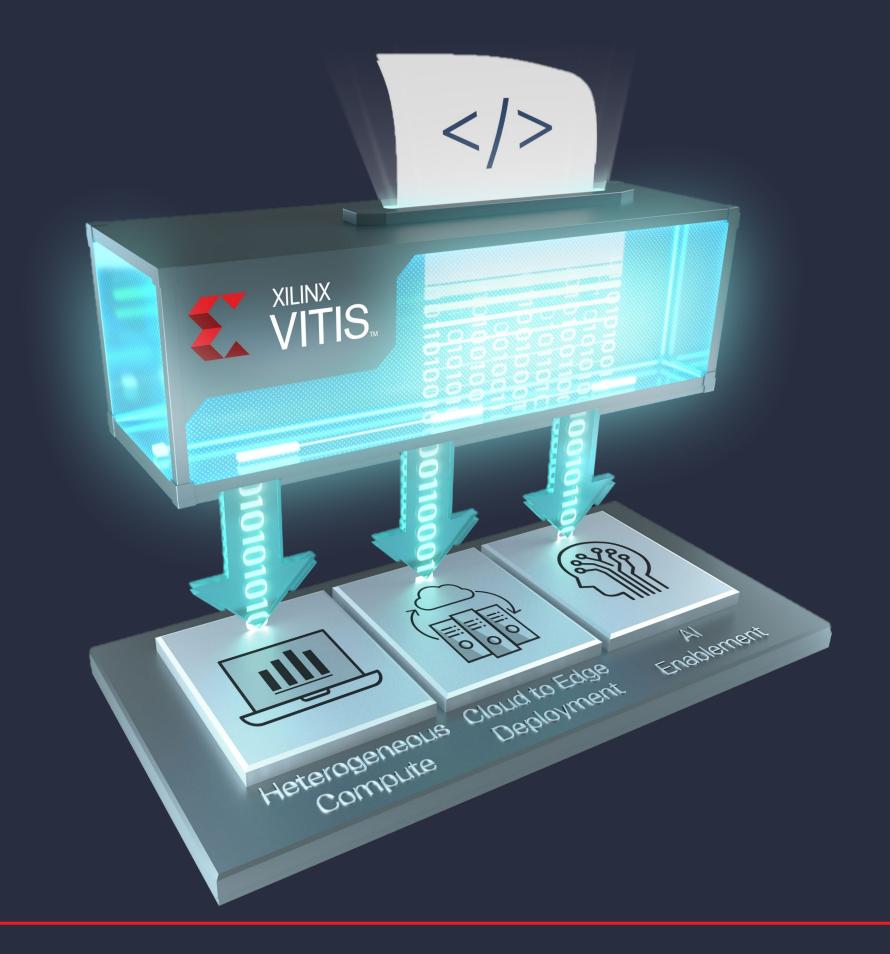
Case Study: Sensor Fusion Architecture

Before vs. After Introduction of FPGA Solution











Thank You



Building the Adaptable, Intelligent World