

Optimierter Code unter Verwendung von ARM-GCC 11.2.1:

```
assembly
    bics    r3, r3, #3
    bxeq    lr
    add     r3, r0, r3, lsl #2
.L8:
    vld1.32 {q8}, [r0]
    vld1.32 {q10}, [r2]!
    vld1.32 {q9}, [r1]!
    vmla.i32 q8, q10, q9
    vst1.32 {q8}, [r0]!
    cmp     r3, r0
    bne     .L8
    bx      lr
```